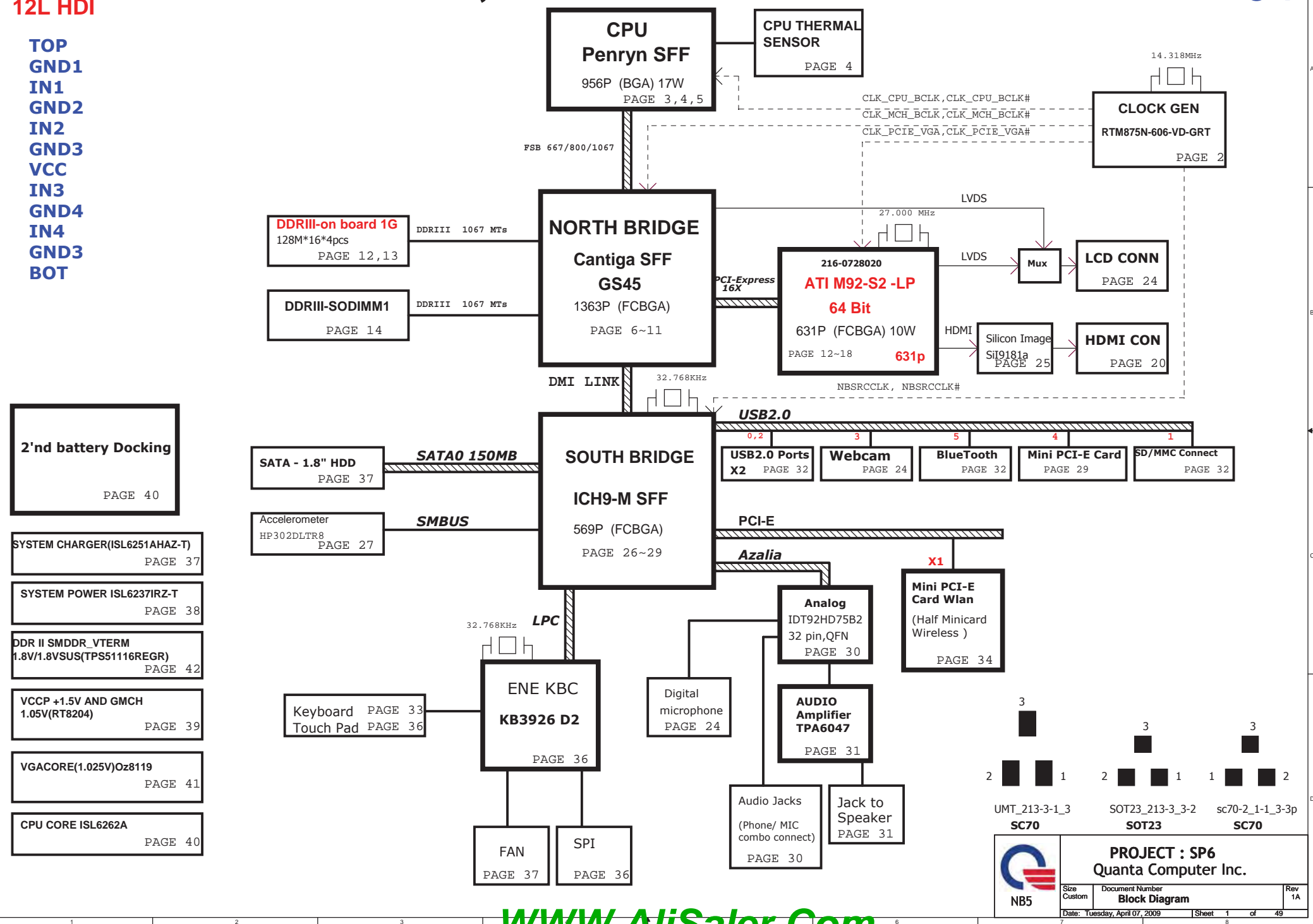
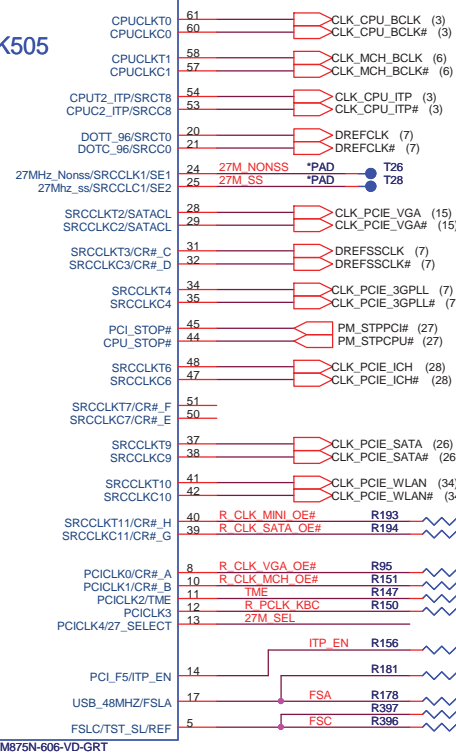
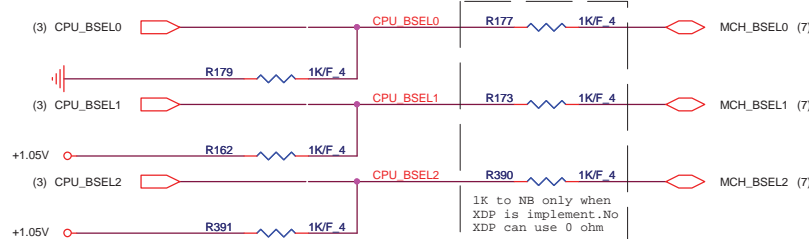
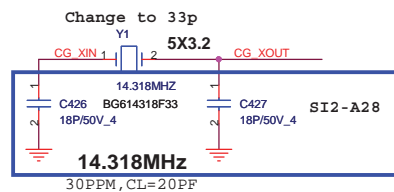
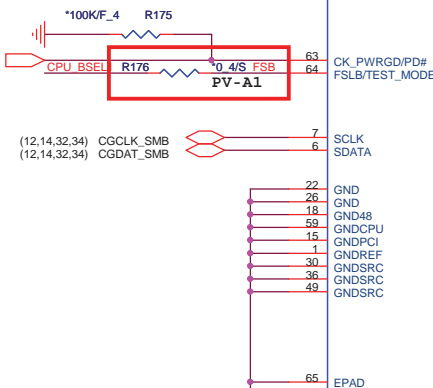
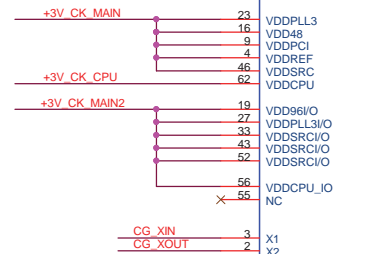


Bond, SP6 BLOCK DIAGRAM

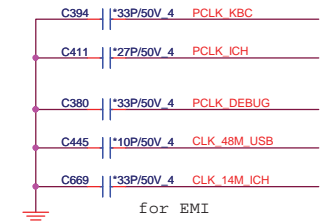




CK505
Realtek
Silego

QFN64

RTM875N-606-VD-GR	AL000875000
SLG8SP513VTR	AL8SP513000

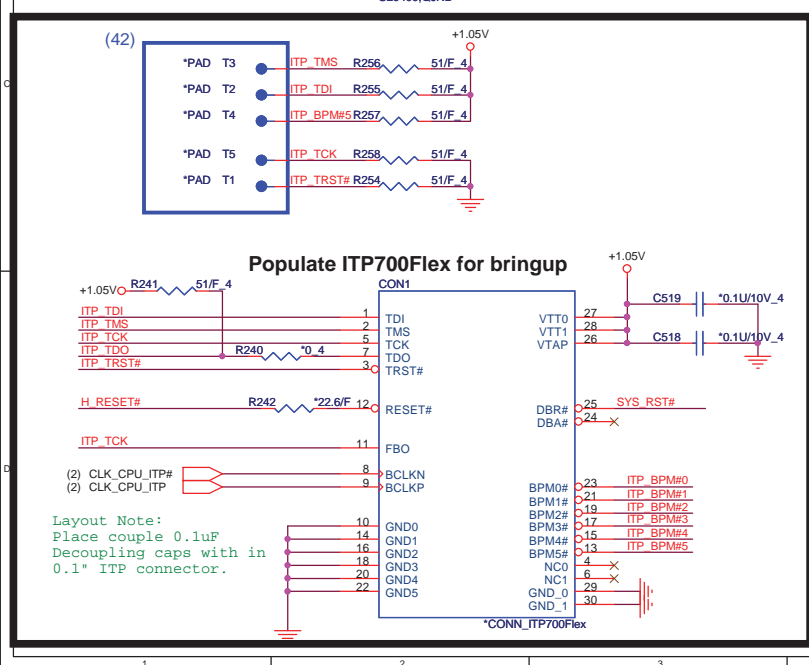
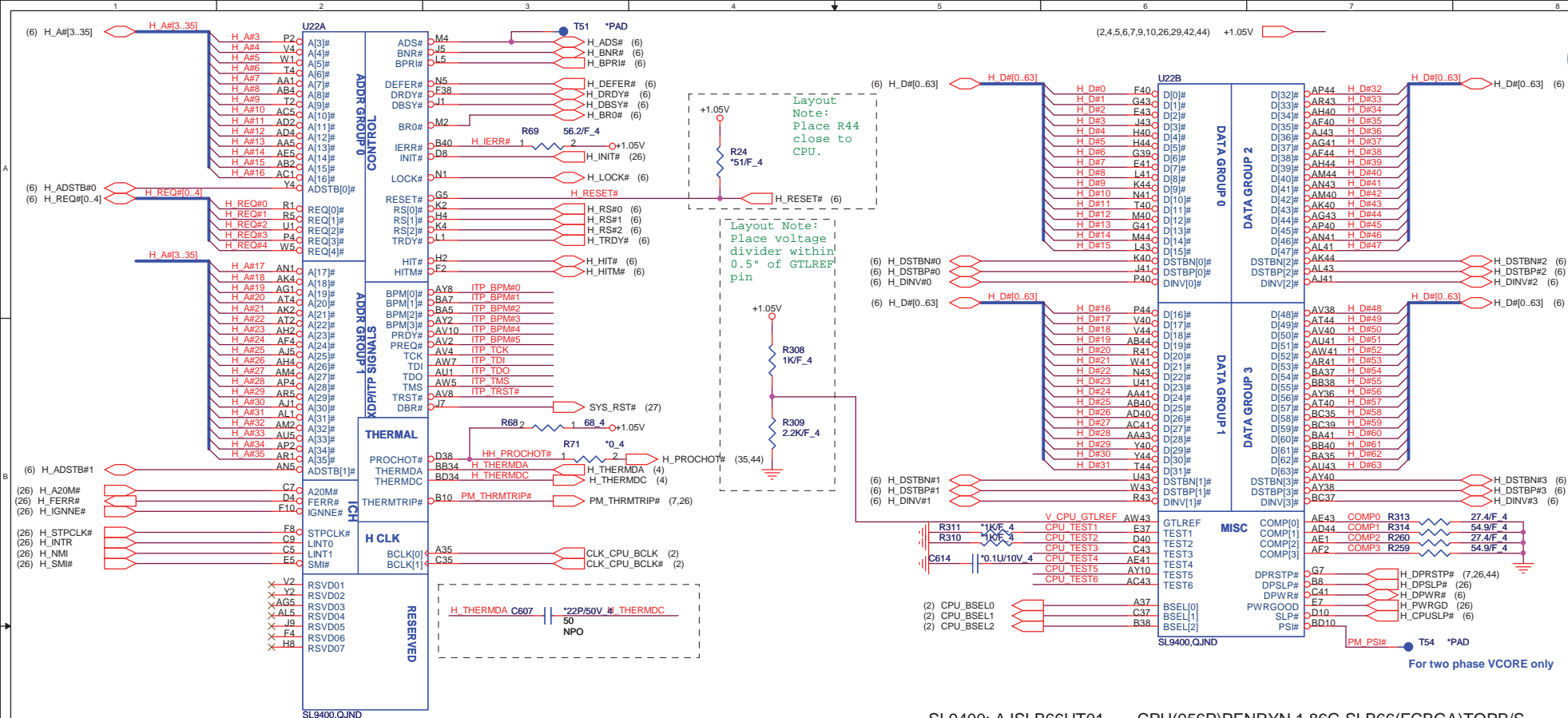


	FSC	FSB	FSA	CPU	SRC	PC
1)	1	0	1	100	100	33
	0	0	1	133	100	33
	0	1	1	166	100	33
	0	1	0	200	100	33
7)	0	0	0	266	100	33
	1	0	0	333	100	33
	1	1	0	400	100	33
7)	1	1	1	RSVD	100	33



PROJECT : SP6
Quanta Computer Inc.

Size Custom	Document Number Clock Generator	Rev 1A
Date: Tuesday, April 07, 2009	Sheet 2 of 49	



SL9400: AJSLB66UT01
SL9400: AJSLB66UT02

SL9600: AJSLGEQUT01
SL9600: AJSLGEQUT00

CPU(956P)PENRYN 1.86G SLB66(FCBGA)TOPB/S
CPU(956P)PENRYN 1.86G SLB66(FCBGA)

CPU(956P)PENRYN 2.13G SLGEQ(BGA)TOPB/S
CPU(956P)PENRYN 2.13G SLGEQ(BGA)

*PAD T78 CPU TEST3
*PAD T53 CPU TEST5
*PAD T77 CPU TEST16
For the purpose of testability, route these signals through a ground referenced Z0 = 55ohm trace that ends in a via that is near a GND via and is accessible through an oscilloscope connection.

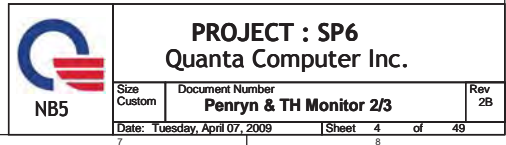
Signal ITP disable guidelines

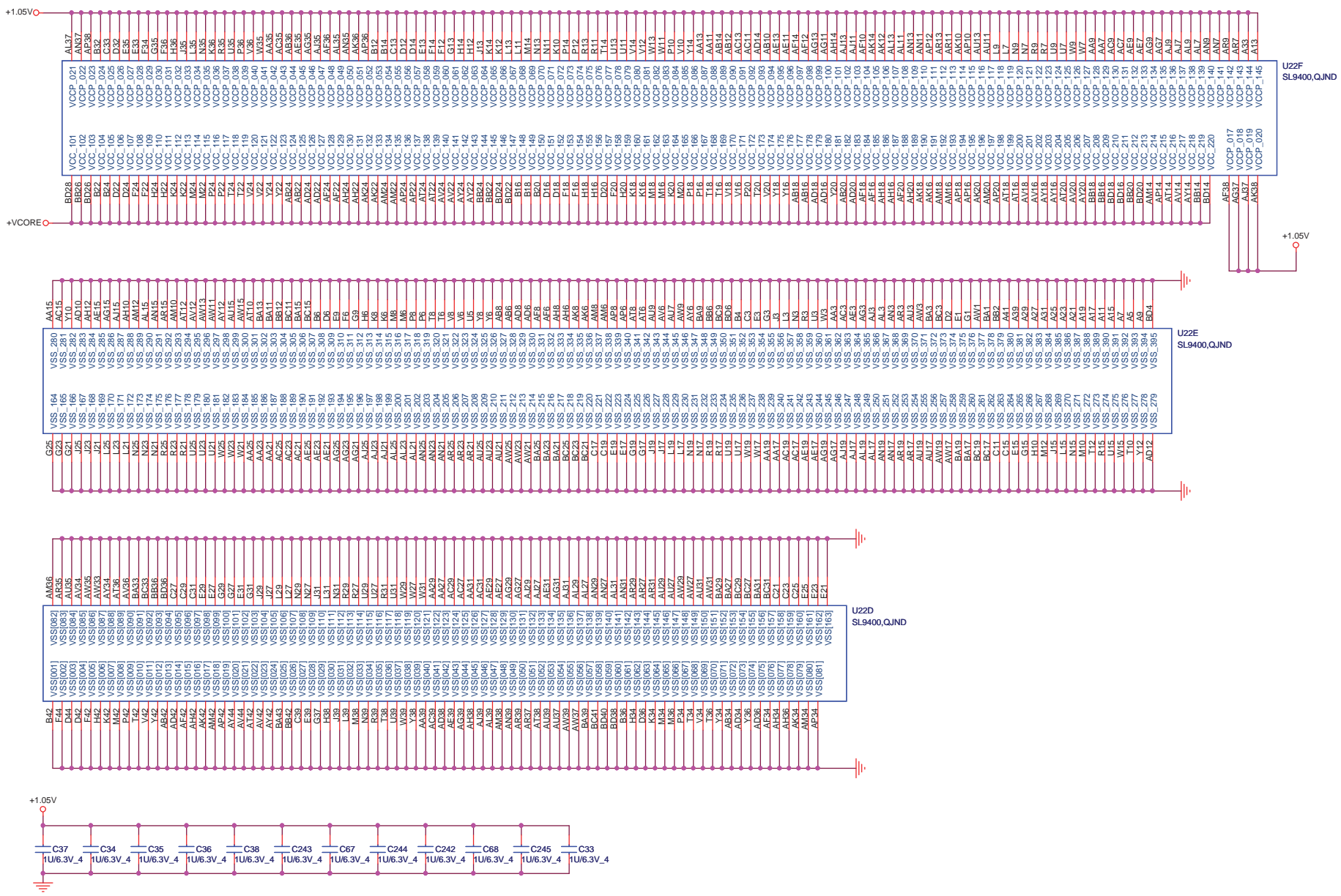
Signal	ITP disable guidelines
TDI	Resistor Value
TMS	150 ohm +/- 5%
TRST#	39 ohm +/- 5%
TCK	680 ohm +/- 5%
TDO	27 ohm +/- 5%
ITP_EN	
	R268 Depop
	+3VVRUN
	Close to CK410M Pin8



PROJECT : SP6
Quanta Computer Inc.

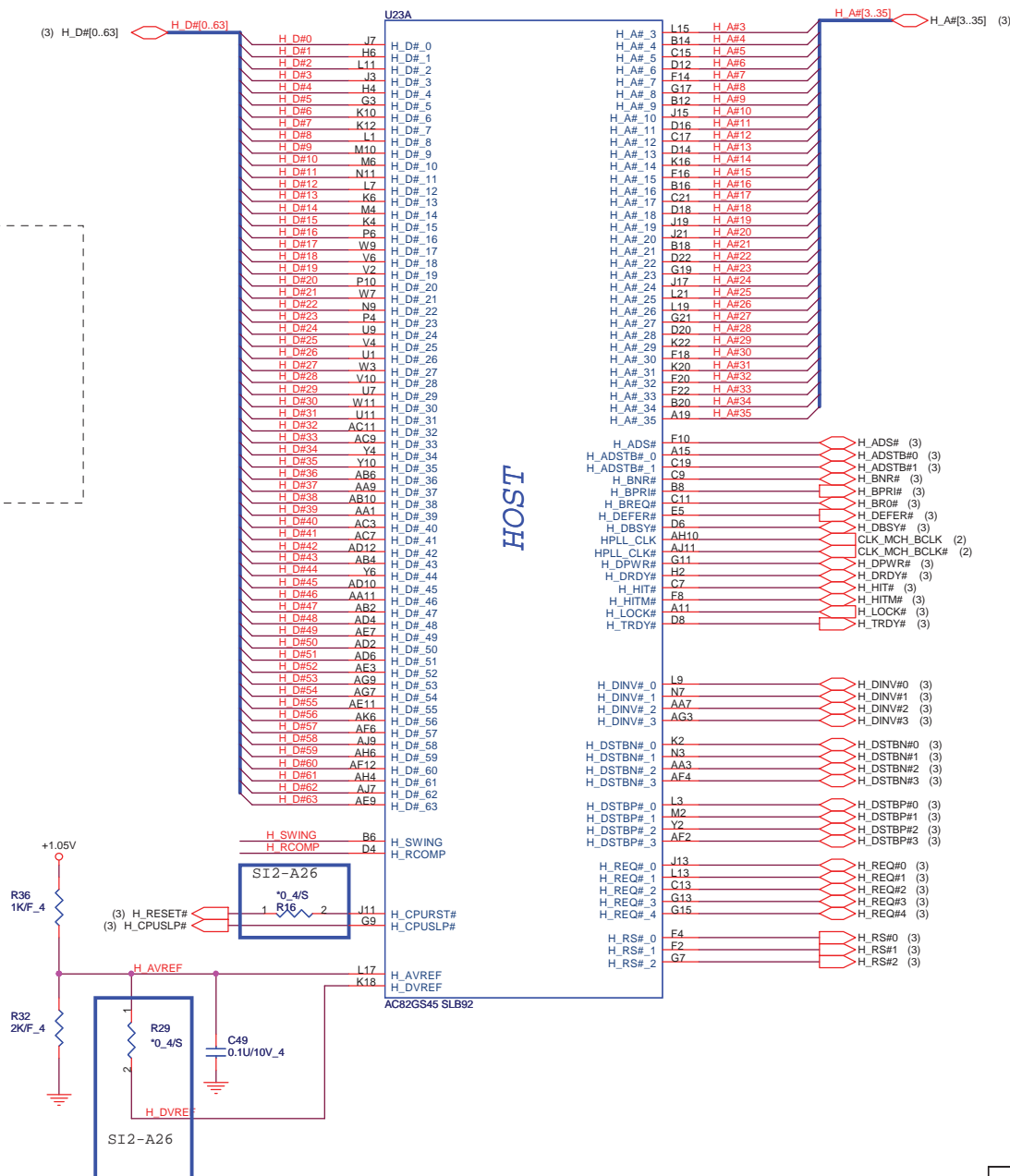
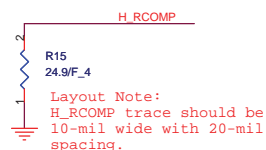
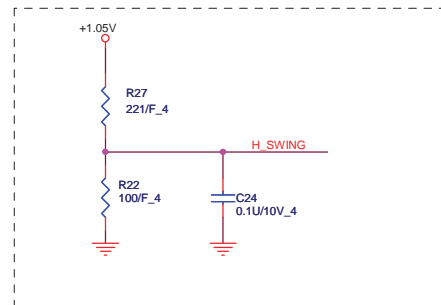
Size Custom Document Number Penryn (HOST BUS) 1/3 Rev 1A
Date: Tuesday, April 07, 2009 Sheet 3 of 49





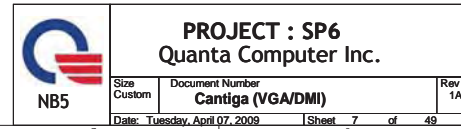
PROJECT : SP6
Quanta Computer Inc.

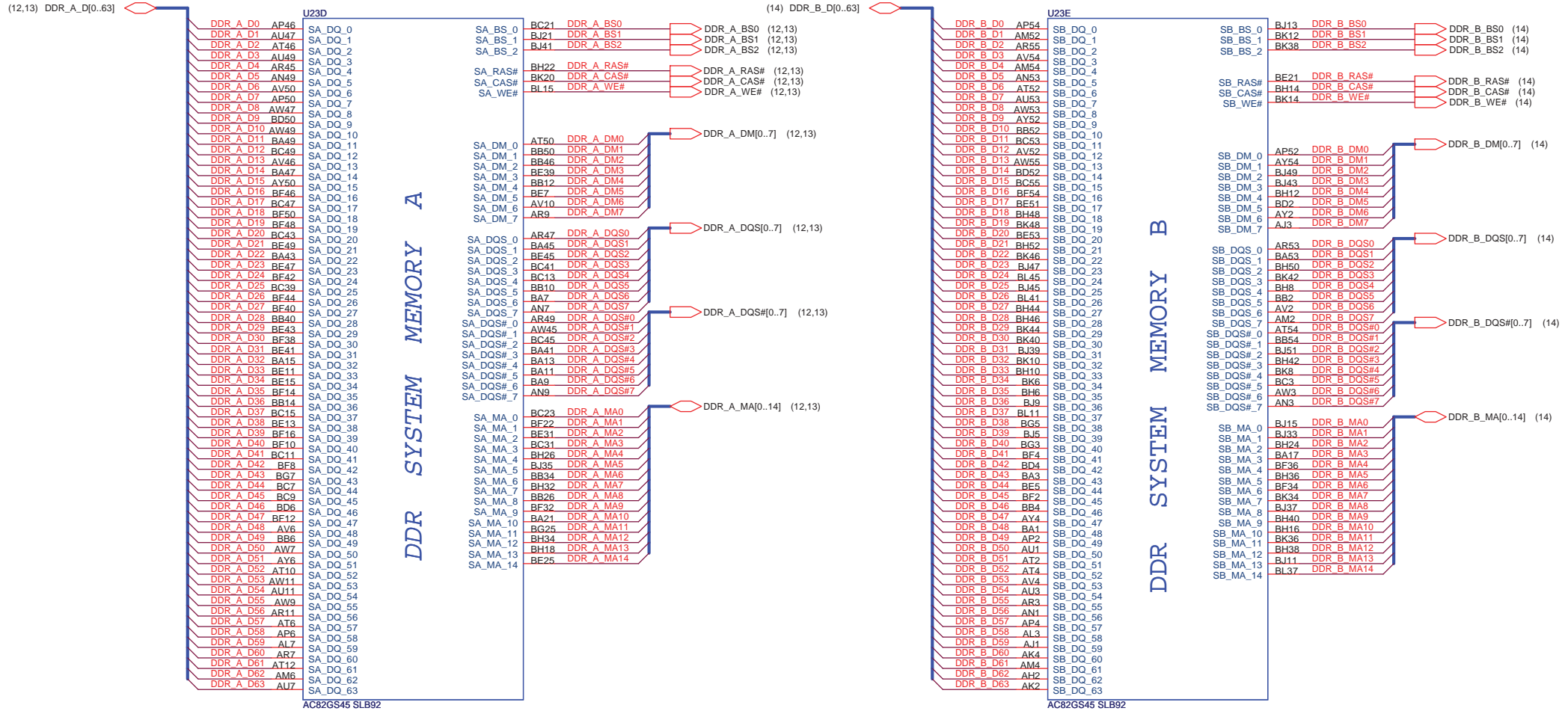
Size Custom	Document Number Penryn 3/3	Rev 1A
Date: Tuesday, April 07, 2009	Sheet 5 of 49	



PROJECT : SP6
Quanta Computer Inc.

Size Custom	Document Number Cantiga hot	Rev 1A
Date: Tuesday, April 07, 2009	Sheet 6 of 49	



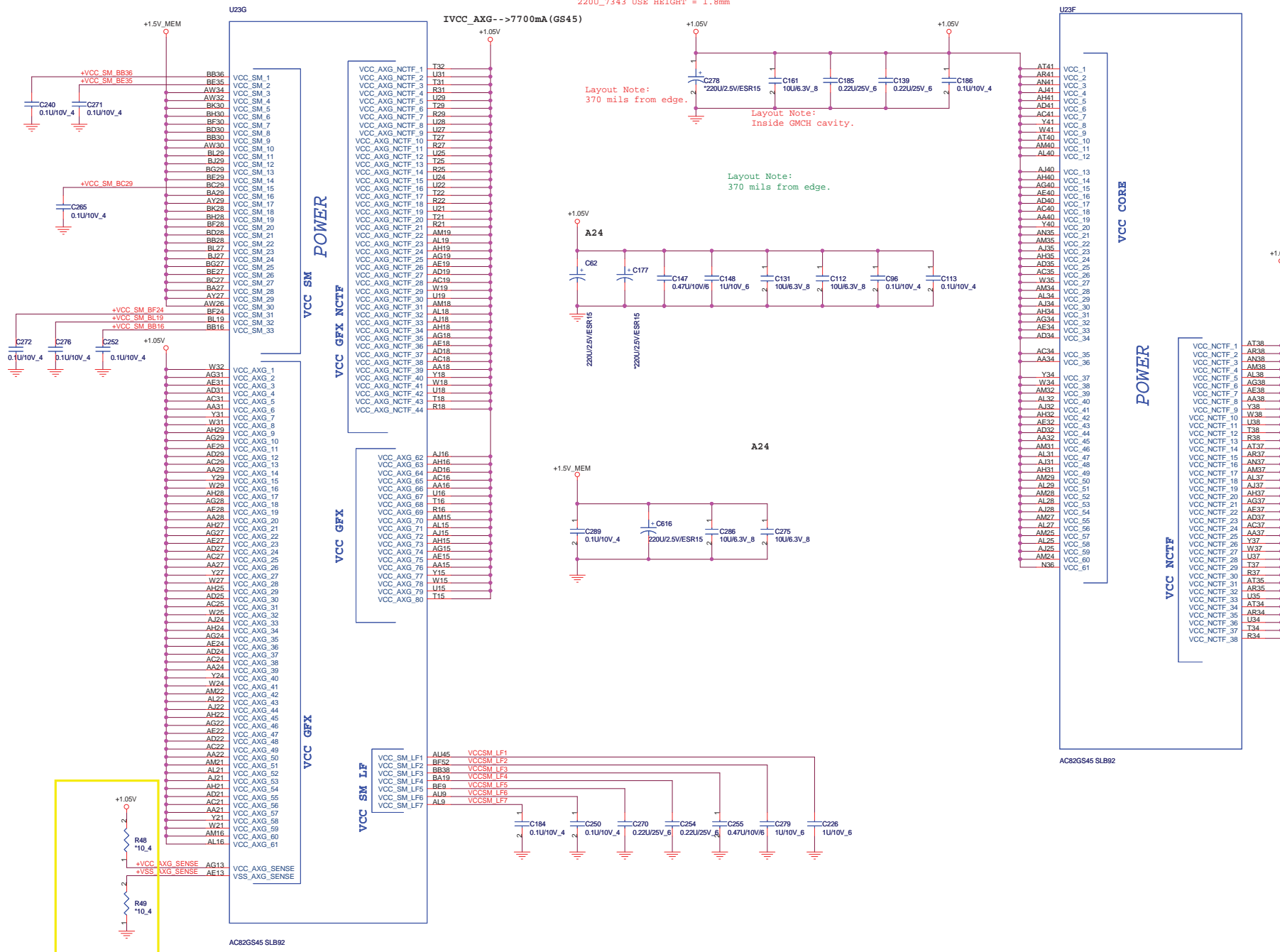


	PROJECT : SP6		Rev 1A
	Quanta Computer Inc.		
	Size Custom	Document Number Cantiga (DDR interface)	
Date: Tuesday, April 07, 2009		Sheet 8 of 49	

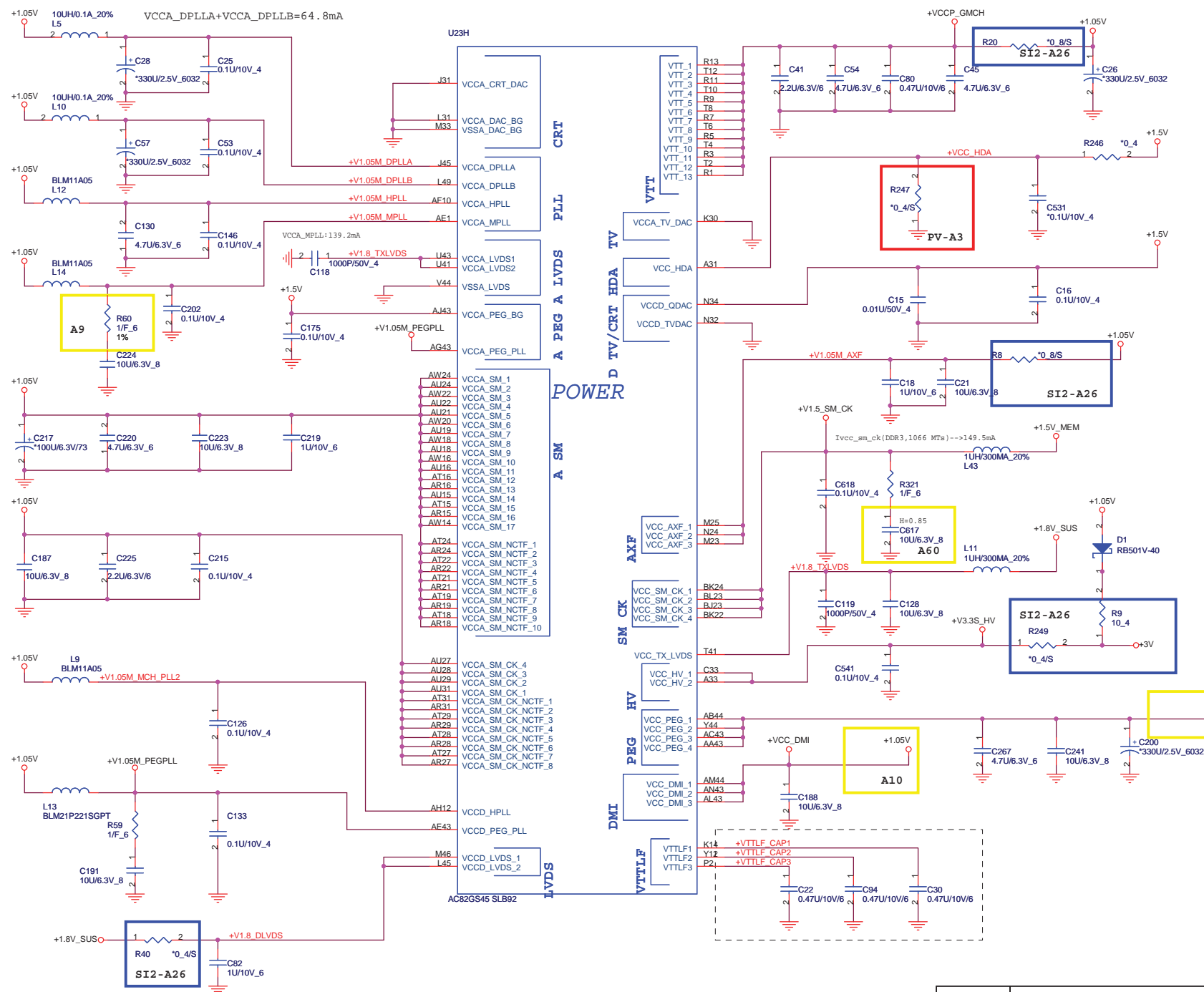
Ivcc_sm (DDR3, 1.5V, 1066MTs) -->4140mA

220U_7343 USE HEIGHT = 1.8mm

IVCC_AXG-->7700mA (GS45)



render standby feature is not implemented can be left



U23I		
BA55	VSS_1	VSS_100
AU55	VSS_2	VSS_101
AN55	VSS_3	VSS_102
AJ55	VSS_4	VSS_103
AE55	VSS_5	VSS_104
AA55	VSS_6	VSS_105
U55	VSS_7	VSS_106
N55	VSS_8	VSS_107
BD54	VSS_9	VSS_108
BG53	VSS_10	VSS_109
AJ53	VSS_11	VSS_110
AE53	VSS_12	VSS_111
AA53	VSS_13	VSS_112
U53	VSS_14	VSS_113
N53	VSS_15	VSS_114
J53	VSS_16	VSS_115
G53	VSS_17	VSS_116
E53	VSS_18	VSS_117
K52	VSS_19	VSS_118
BG51	VSS_20	VSS_119
BA51	VSS_21	VSS_120
AW51	VSS_22	VSS_121
AU51	VSS_23	VSS_122
AR51	VSS_24	VSS_123
AN51	VSS_25	VSS_124
AL51	VSS_26	VSS_125
AJ51	VSS_27	VSS_126
AG51	VSS_28	VSS_127
AE51	VSS_29	VSS_128
AC51	VSS_30	VSS_129
AA51	VSS_31	VSS_130
W51	VSS_32	VSS_131
U51	VSS_33	VSS_132
R51	VSS_34	VSS_133
N51	VSS_35	VSS_134
L51	VSS_36	VSS_135
J51	VSS_37	VSS_136
G51	VSS_38	VSS_137
C51	VSS_39	VSS_138
BK50	VSS_40	VSS_139
AM50	VSS_41	VSS_140
K50	VSS_42	VSS_141
BG49	VSS_43	VSS_142
E49	VSS_44	VSS_143
C49	VSS_45	VSS_144
BD48	VSS_46	VSS_145
BB48	VSS_47	VSS_146
AV48	VSS_48	VSS_147
AY48	VSS_49	VSS_148
AT48	VSS_50	VSS_149
AP48	VSS_51	VSS_150
AM48	VSS_52	VSS_151
AK48	VSS_53	VSS_152
AH48	VSS_54	VSS_153
AF48	VSS_55	VSS_154
AD48	VSS_56	VSS_155
AB48	VSS_57	VSS_156
Y48	VSS_58	VSS_157
V48	VSS_59	VSS_158
T48	VSS_60	VSS_159
P48	VSS_61	VSS_160
M48	VSS_62	VSS_161
K48	VSS_63	VSS_162
H48	VSS_64	VSS_163
BL47	VSS_65	VSS_164
BG47	VSS_66	VSS_165
E47	VSS_67	VSS_166
C47	VSS_68	VSS_167
A47	VSS_69	VSS_168
BD46	VSS_70	VSS_169
AY46	VSS_71	VSS_170
AM46	VSS_72	VSS_171
AK46	VSS_73	VSS_172
AH46	VSS_74	VSS_173
AG46	VSS_75	VSS_174
AE46	VSS_76	VSS_175
AC46	VSS_77	VSS_176
AA46	VSS_78	VSS_177
W45	VSS_79	VSS_178
R45	VSS_80	VSS_179
N45	VSS_81	VSS_180
E45	VSS_82	VSS_181
BD44	VSS_83	VSS_182
BB44	VSS_84	VSS_183
AV44	VSS_85	VSS_184
AY44	VSS_86	VSS_185
AH44	VSS_87	VSS_186
AF44	VSS_88	VSS_187
AD44	VSS_89	VSS_188
K44	VSS_90	VSS_189
H44	VSS_91	VSS_190
BL43	VSS_92	VSS_191
BG43	VSS_93	VSS_192
AY43	VSS_94	VSS_193
AR43	VSS_95	VSS_194
W43	VSS_96	VSS_195
R43	VSS_97	VSS_196
M43	VSS_98	VSS_197
E43	VSS_99	VSS_198

AC82GS45 SLB92

U23J		
AN25	VSS_199	VSS_300
AG25	VSS_200	VSS_301
AE25	VSS_201	VSS_302
AA25	VSS_202	VSS_303
Y25	VSS_203	VSS_304
E25	VSS_204	VSS_305
A25	VSS_205	VSS_306
BD24	VSS_206	VSS_307
AN24	VSS_207	VSS_308
AL24	VSS_208	VSS_309
H24	VSS_209	VSS_310
BG23	VSS_210	VSS_311
AY23	VSS_211	VSS_312
E23	VSS_212	VSS_313
BD22	VSS_213	VSS_314
E41	VSS_214	VSS_315
AN22	VSS_215	VSS_316
Y22	VSS_216	VSS_317
W22	VSS_217	VSS_318
H22	VSS_218	VSS_319
BL21	VSS_219	VSS_320
BG21	VSS_220	VSS_321
AY21	VSS_221	VSS_322
AN21	VSS_222	VSS_323
AG21	VSS_223	VSS_324
AE21	VSS_224	VSS_325
M21	VSS_225	VSS_326
E21	VSS_226	VSS_327
A21	VSS_227	VSS_328
BD20	VSS_228	VSS_329
H20	VSS_229	VSS_330
BG19	VSS_230	VSS_331
AY19	VSS_231	VSS_332
M19	VSS_232	VSS_333
E19	VSS_233	VSS_334
BD18	VSS_234	VSS_335
N18	VSS_235	VSS_336
H18	VSS_236	VSS_337
BL17	VSS_237	VSS_338
BG17	VSS_238	VSS_339
AY17	VSS_239	VSS_340
M17	VSS_240	VSS_341
E17	VSS_241	VSS_342
BD16	VSS_242	VSS_343
AY16	VSS_243	VSS_344
AN16	VSS_244	VSS_345
AG16	VSS_245	VSS_346
AE16	VSS_246	VSS_347
Y16	VSS_247	VSS_348
W16	VSS_248	VSS_349
N16	VSS_249	VSS_350
H16	VSS_250	VSS_351
BG15	VSS_251	VSS_352
AY15	VSS_252	VSS_353
AN15	VSS_253	VSS_354
AD15	VSS_254	VSS_355
AC15	VSS_255	VSS_356
R15	VSS_256	VSS_357
M15	VSS_257	VSS_358
E15	VSS_258	VSS_359
BD14	VSS_259	VSS_360
H14	VSS_260	VSS_361
BL13	VSS_261	VSS_362
BG13	VSS_262	VSS_363
AY13	VSS_263	VSS_364
AN13	VSS_264	VSS_365
AR13	VSS_265	VSS_366
AJ13	VSS_266	VSS_367
AC13	VSS_267	VSS_368
AA13	VSS_268	VSS_369
W13	VSS_269	VSS_370
U13	VSS_270	VSS_371
M13	VSS_271	VSS_372
A13	VSS_272	VSS_373
BD12	VSS_273	VSS_374
AV12	VSS_274	VSS_375
AP12	VSS_276	VSS_377
AM12	VSS_277	VSS_378
AK12	VSS_278	VSS_379
AB12	VSS_279	VSS_380
V12	VSS_280	VSS_381
P12	VSS_281	VSS_382
H12	VSS_282	VSS_383
BG11	VSS_283	VSS_384
AG11	VSS_284	VSS_385
E11	VSS_285	VSS_386
BD10	VSS_286	VSS_387
AY10	VSS_287	VSS_388
AP10	VSS_288	VSS_389
H10	VSS_289	VSS_390
BL9	VSS_290	VSS_391
BG9	VSS_291	VSS_392
E9	VSS_292	VSS_393
BD8	VSS_293	VSS_394
BD8	VSS_294	VSS_395
BB8	VSS_296	VSS_396
AY8	VSS_297	VSS_397
AV8	VSS_298	VSS_398
AT8	VSS_299	VSS_399
AP8	VSS_300	VSS_400

VSS

VSS NCTF

VSS SCB

VSS NCTF_1
VSS NCTF_2
VSS NCTF_3
VSS NCTF_4
VSS NCTF_5
VSS NCTF_6
VSS NCTF_7
VSS NCTF_8
VSS NCTF_9
VSS NCTF_10
VSS NCTF_11
VSS NCTF_12
VSS NCTF_13
VSS NCTF_14
VSS NCTF_15
VSS NCTF_16
VSS NCTF_17
VSS NCTF_18
VSS NCTF_19
VSS NCTF_20
VSS NCTF_21
VSS NCTF_22
VSS NCTF_23

VSS SCB_1
VSS SCB_2
VSS SCB_3
VSS SCB_4
VSS SCB_5
VSS SCB_6
VSS SCB_7

AM8	VSS_300
AK8	VSS_301
AH8	VSS_302
AF8	VSS_303
AD8	VSS_304
AB8	VSS_305
Y8	VSS_306
V8	VSS_307
P8	VSS_308
M8	VSS_309
K8	VSS_310
H8	VSS_311
BJ7	VSS_312
E7	VSS_313
BF6	VSS_314
BC5	VSS_315
BA5	VSS_316
AW5	VSS_317
AU5	VSS_318
AR5	VSS_319
AN5	VSS_320
AL5	VSS_321
AJ5	VSS_322
AG5	VSS_323
AE5	VSS_324
AC5	VSS_325
AA5	VSS_326
W5	VSS_327
U5	VSS_328
N5	VSS_329
L5	VSS_330
J5	VSS_331
G5	VSS_332
C5	VSS_333
BH4	VSS_334
BE3	VSS_335
U3	VSS_336
E3	VSS_337
BC1	VSS_338
AW1	VSS_339
AR1	VSS_340
AL1	VSS_341
AG1	VSS_342
AC1	VSS_343
W1	VSS_344
N1	VSS_345
J1	VSS_346
AU43	VSS_347
BB42	VSS_348
AW38	VSS_349
BA35	VSS_350
L29	VSS_351
N28	VSS_352
N22	VSS_353
N20	VSS_354
N14	VSS_355
AL13	VSS_356
B10	VSS_357
AN13	VSS_358
N42	VSS_359
N40	VSS_360
N38	VSS_361
M39	VSS_362

AJ38
AH38
AD38
AC38
T35
R35
AT32
AR32
U32
R32
T28
R28
AT25
AR25
T24
R24
AN19
AJ19
AA19
Y19
T19
R19
AN18

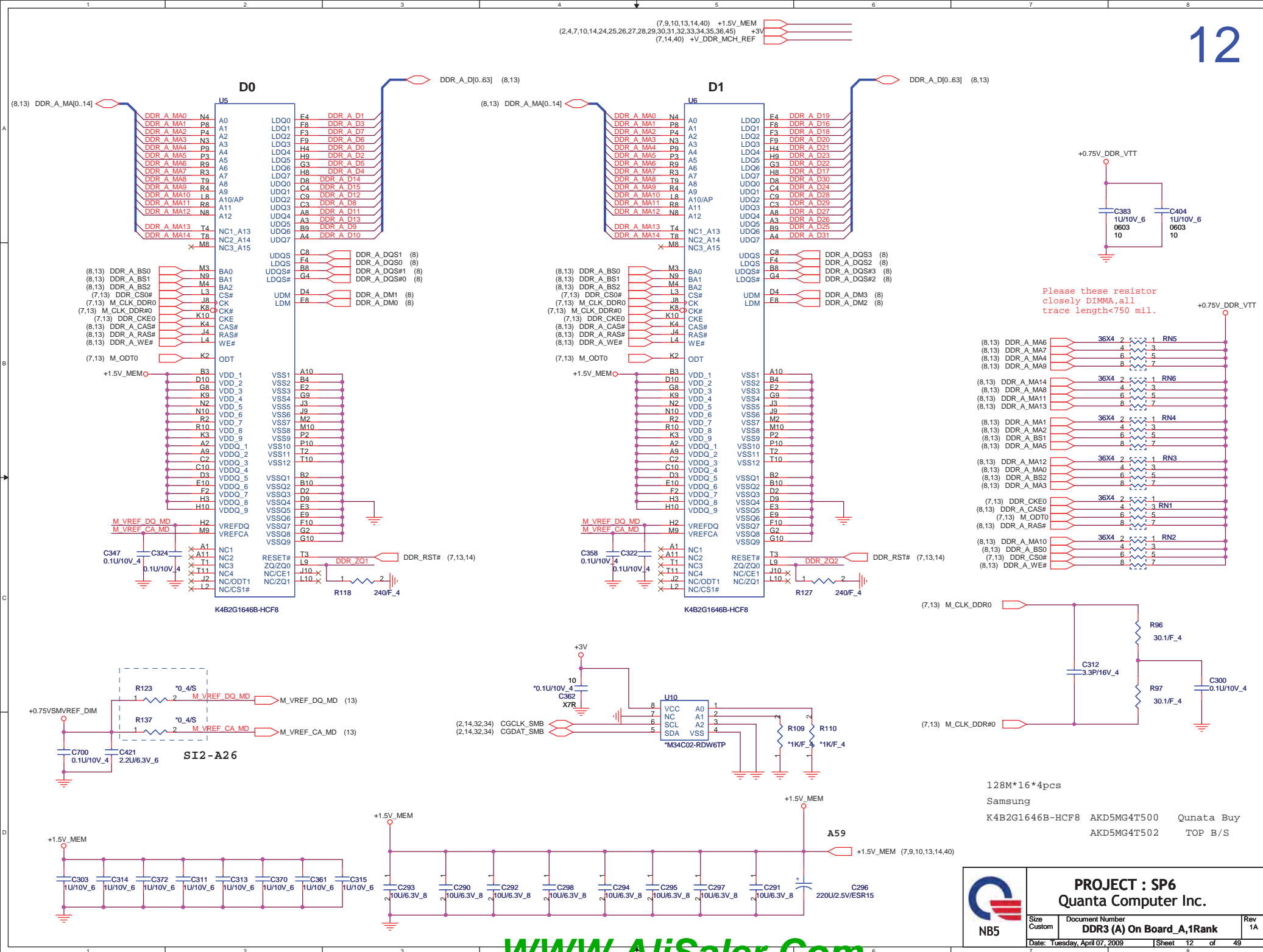
BL55
BL1
A55
D1
B55
B2
A4

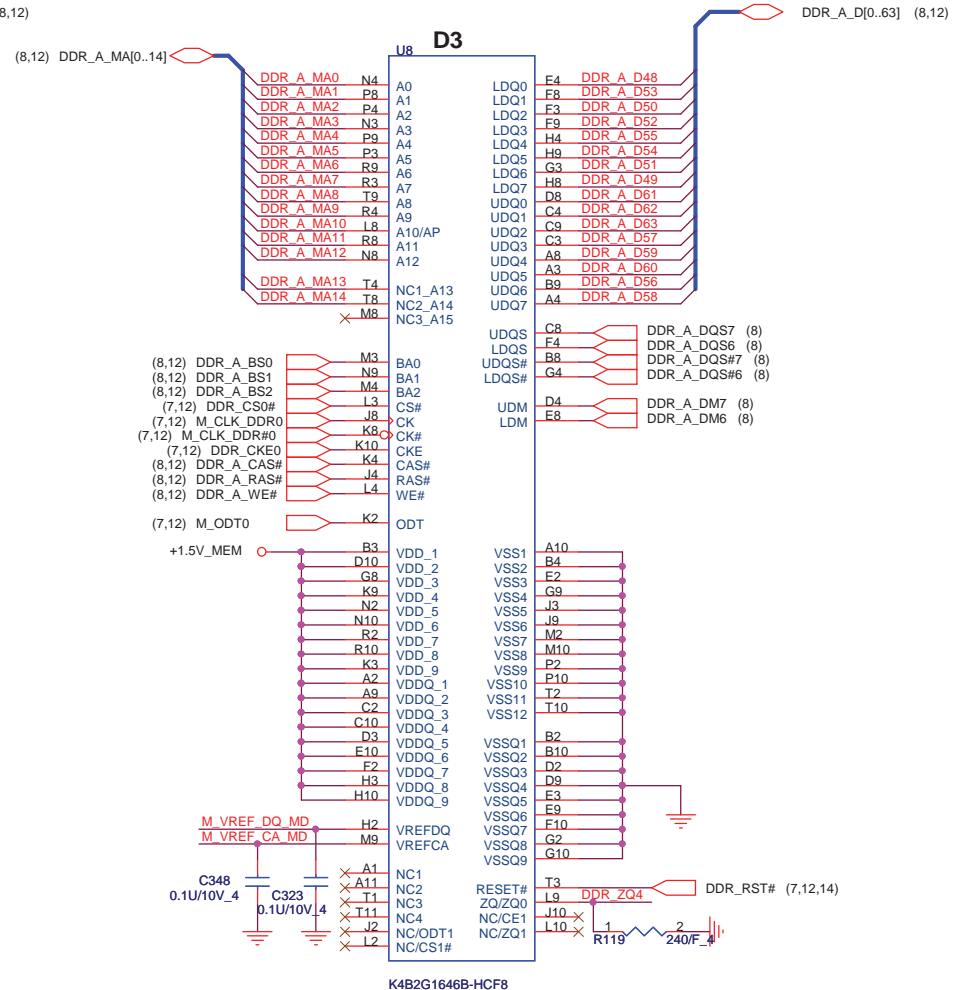
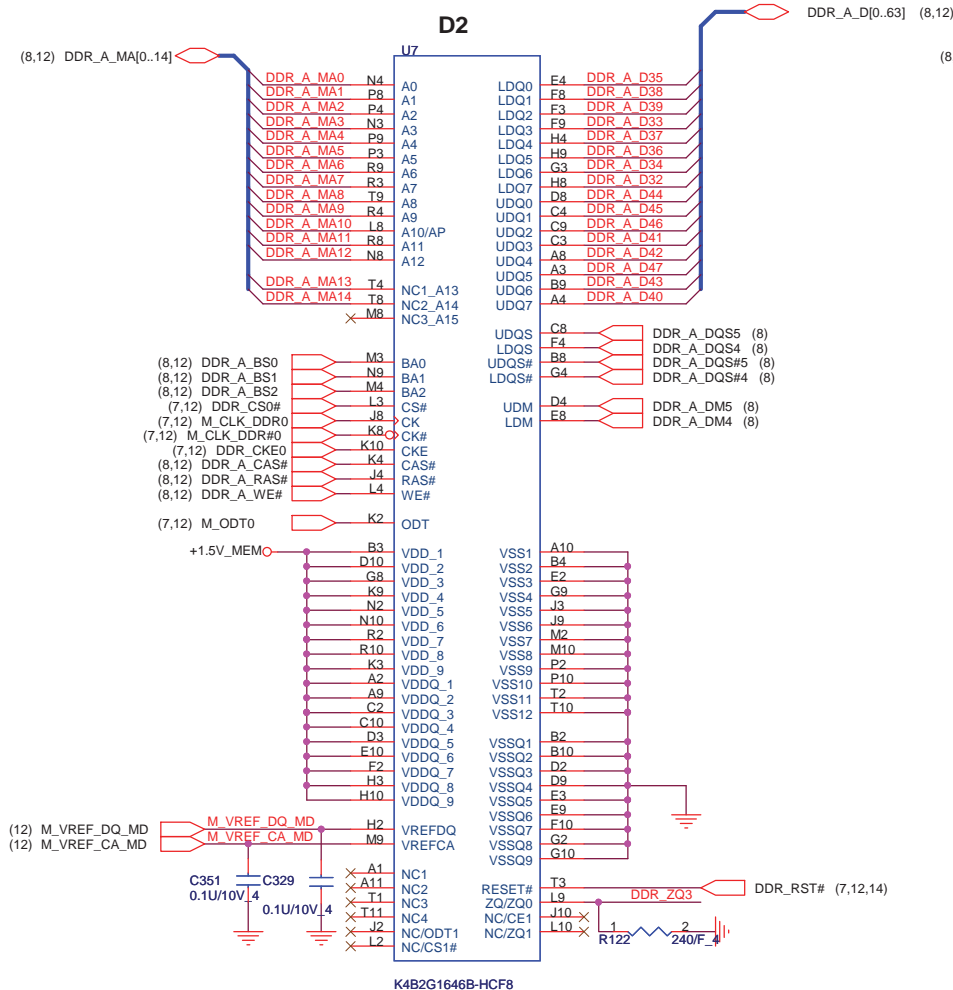
AC82GS45 SLB92



PROJECT : SP6
Quanta Computer Inc.

Size Custom	Document Number Cantiga (Vss)	Rev 1A
Date: Tuesday, April 07, 2009	Sheet 11	of 49



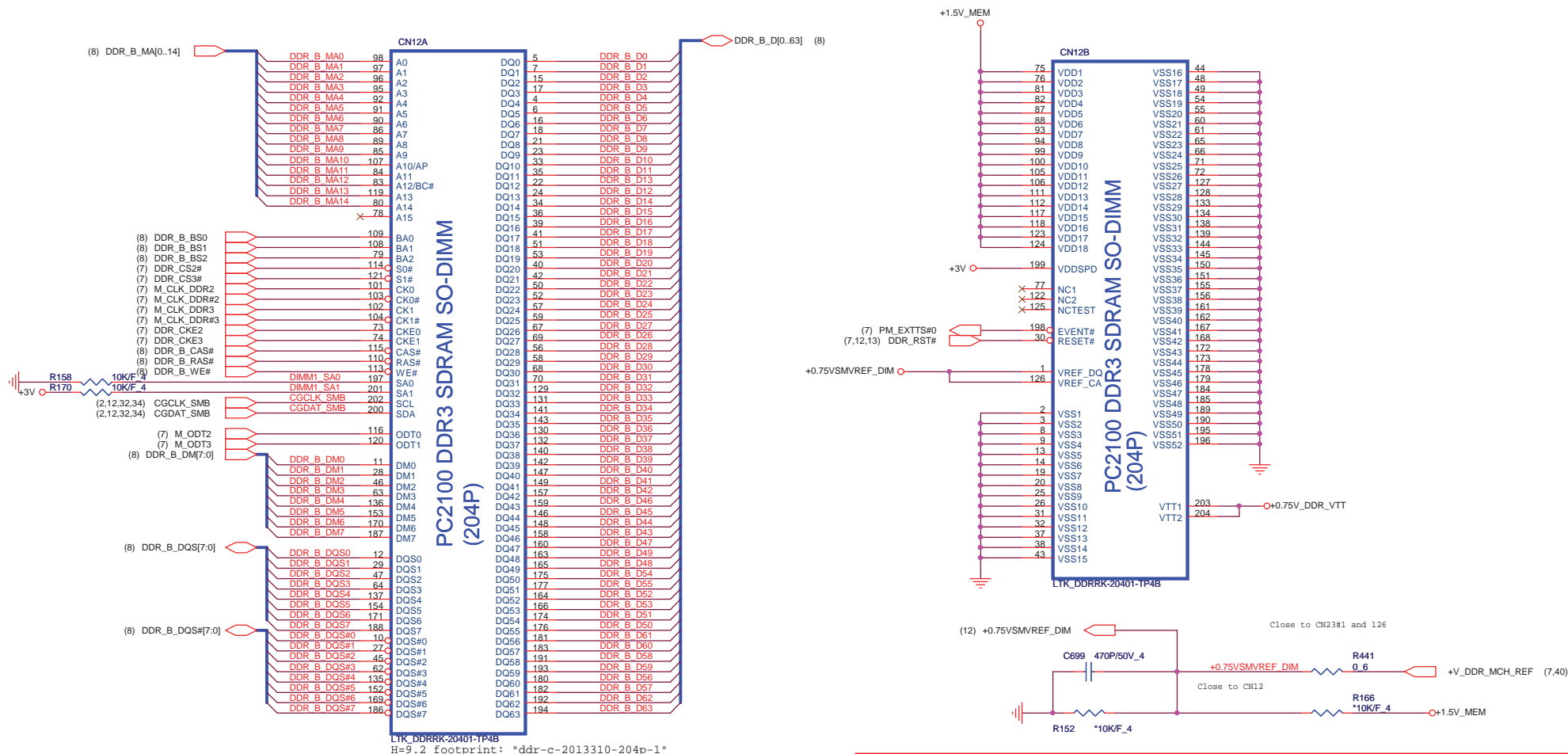


PROJECT : SP6
Quanta Computer Inc.

Size	Document Number	Rev
B	DDR3 (A) On Board_B,1Rank	1A
Date: Tuesday, April 07, 2009	Sheet 13 of 49	

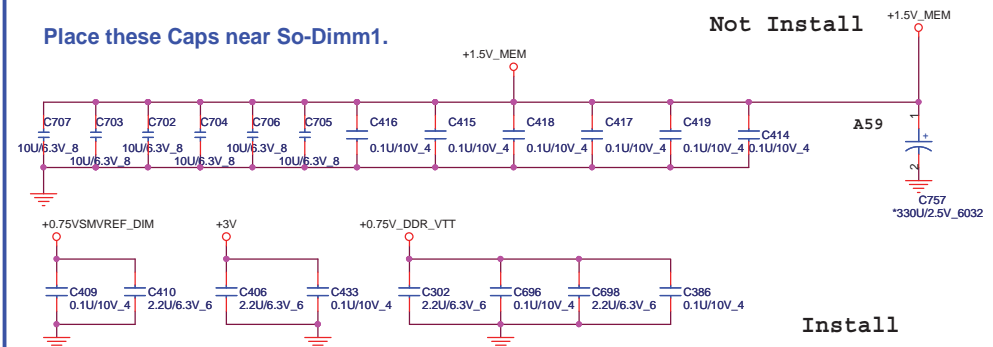
0922-->CN23 update library to DDR-AS0A62X-U4RN-7F-204P type

(7,9,10,12,13,40) +1.5V_MEM
(2,4,7,10,12,24,25,26,27,28,29,30,31,32,33,34,35,36,45) +3V
(12,40) +0.75V_DDR_VTT

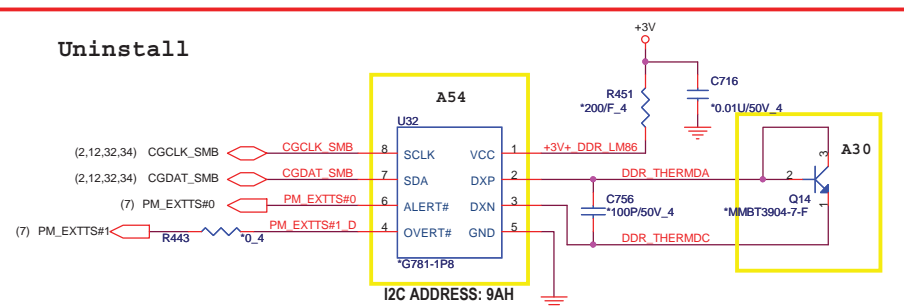


Place these Caps near So-Dimm1.

Not Install



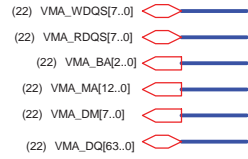
Uninstall



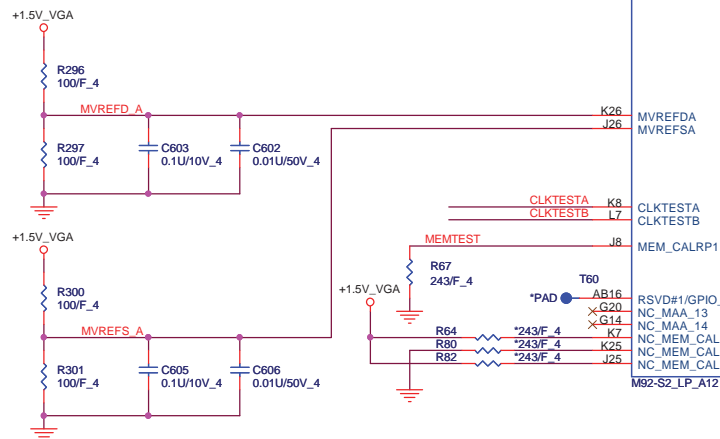
PROJECT : SP6
Quanta Computer Inc.

Size Custom Document Number
DDR3 Connect
Date: Tuesday, April 07, 2009 Sheet 14 of 49

MEMORY INTERFACE

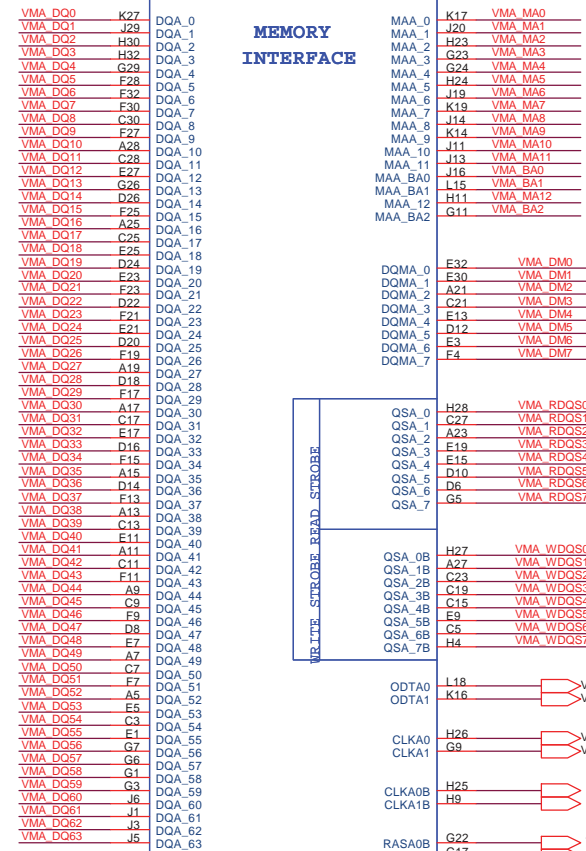


DIVIDER RESISTORS	DDR3
MVREF TO 1.5V	100R
MVREF TO GND	100R



U24C PART 3 OF 10

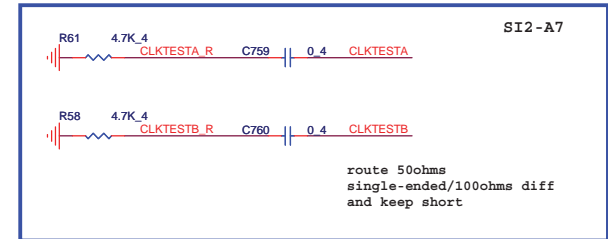
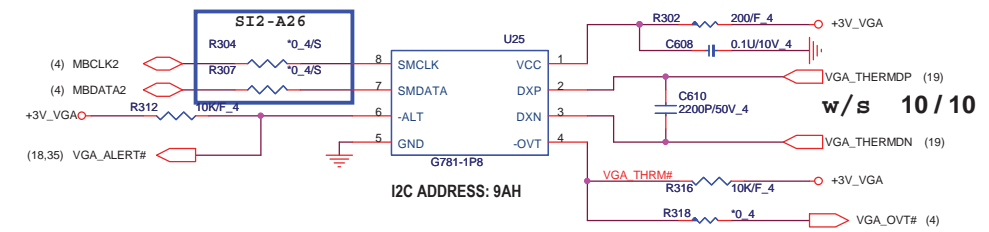
MEMORY INTERFACE



(20,22,43) +1.5V_VGA

(15,18,19,20,25,35,45) +3V_VGA

THERMAL MONITOR



For DDR3

PROJECT : SP6
Quanta Computer Inc.

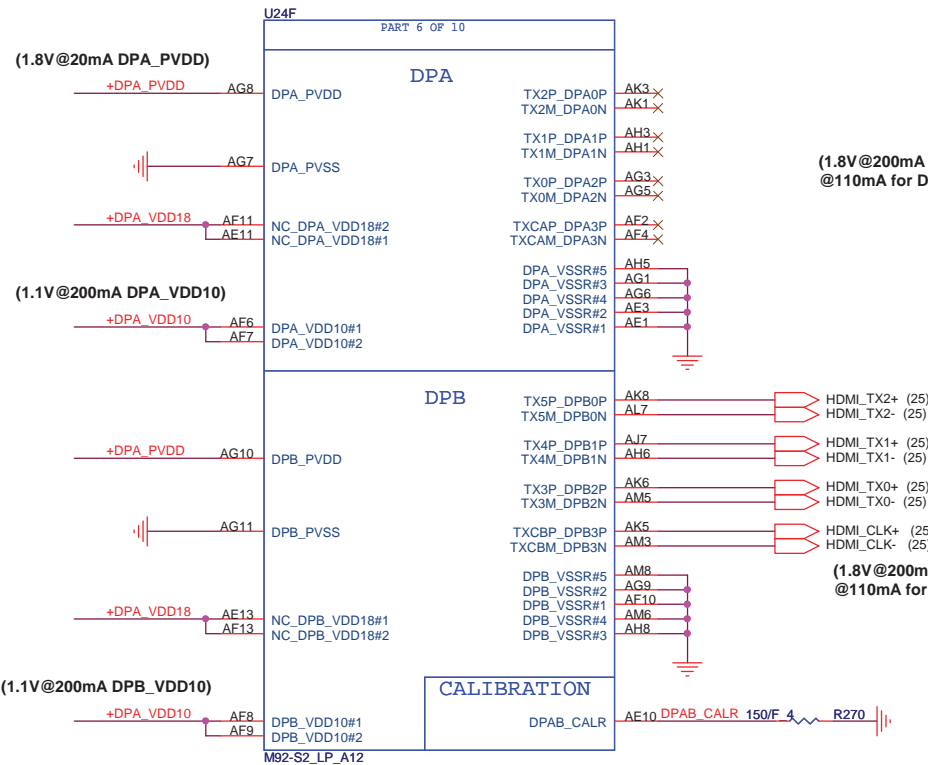
Size Custom	Document Number M92 Memory/Thermal	Rev 1A
Date: Tuesday, April 07, 2009	Sheet 16 of 49	

TMDP(HDMI) INTERFACE

(15,18,19,20,40) +1.1V

LVDS INTERFACE

17



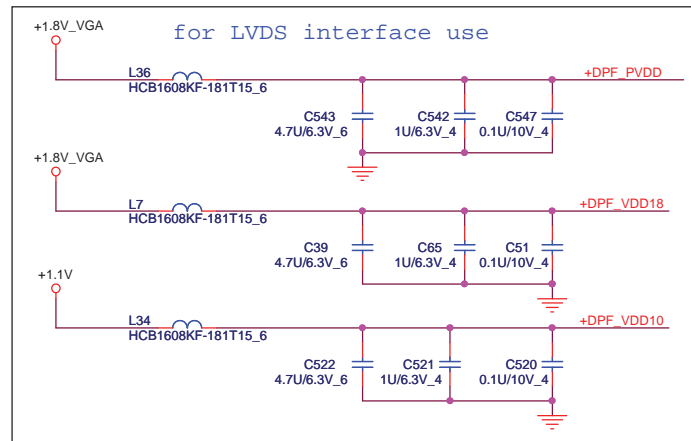
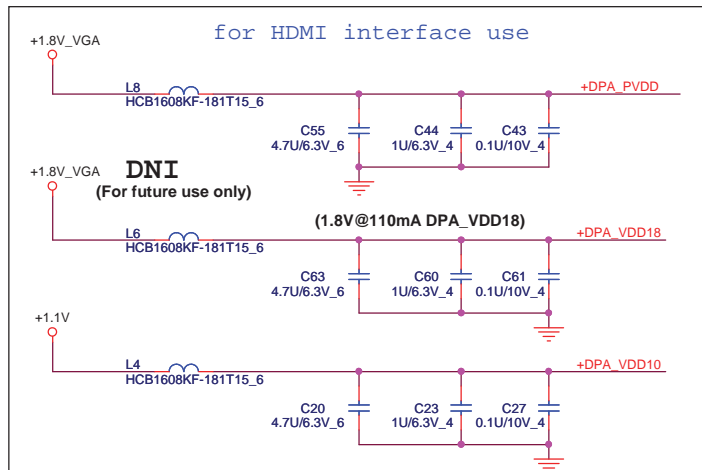
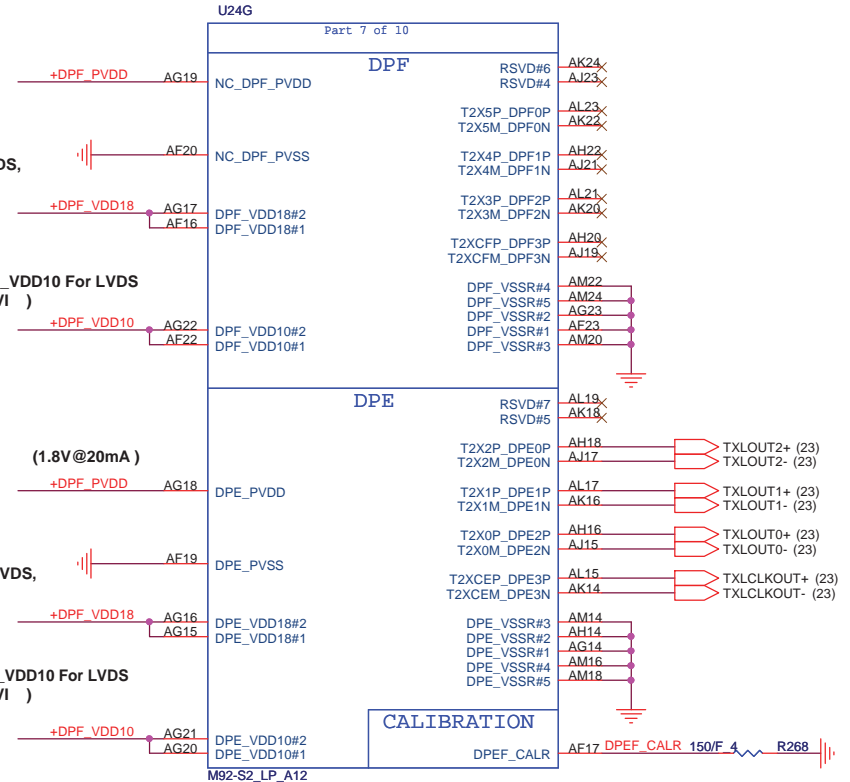
(1.8V@200mA DPF_VDD18 for LVDS,
@110mA for DP/DVI)

(1.1V@100mA DPF_VDD10 For LVDS
@170mA for DP/DVI)

(1.8V@20mA)

(1.8V@200mA DPE_VDD18 for LVDS,
@110mA for DP/DVI)

(1.1V@100mA DPE_VDD10 For LVDS
@170mA for DP/DVI)



PROJECT : SP6
Quanta Computer Inc.

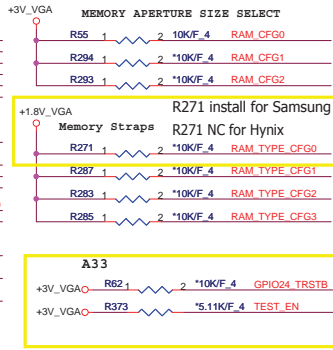
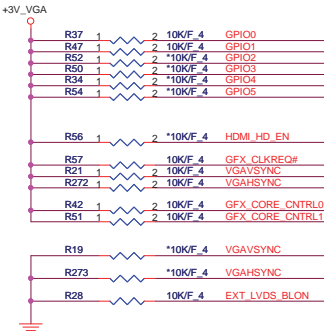
Size	Document Number	Rev
B	M92 HDMI/LVDS interface	1A
Date: Tuesday, April 07, 2009	Sheet 17 of 49	

MEMORY APERTURE SIZE SELECT				
MEMORY SIZE	CFG3 GPIO9	CFG2 GPIO13	CFG1 GPIO12	CFG0 GPIO11
128MB		0	0	0
256MB		0	0	1
512MB		0	1	0
512MB		1	0	0

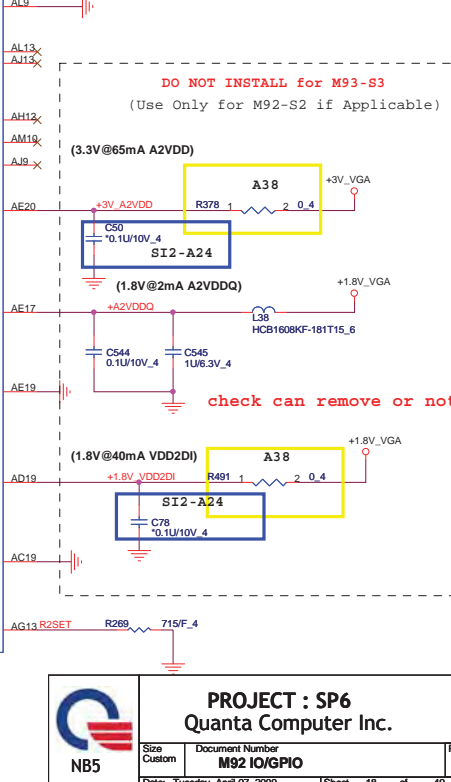
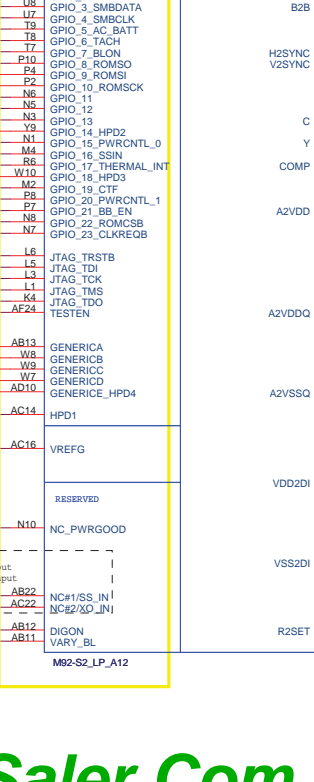
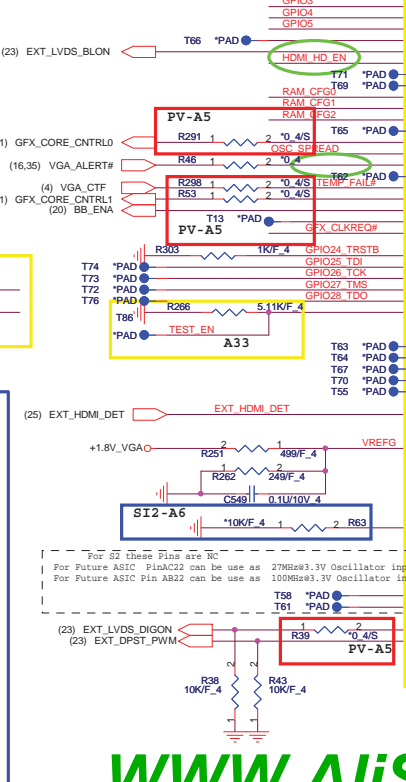
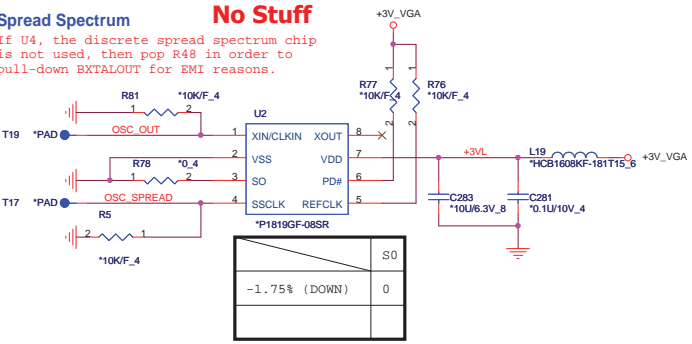
Default

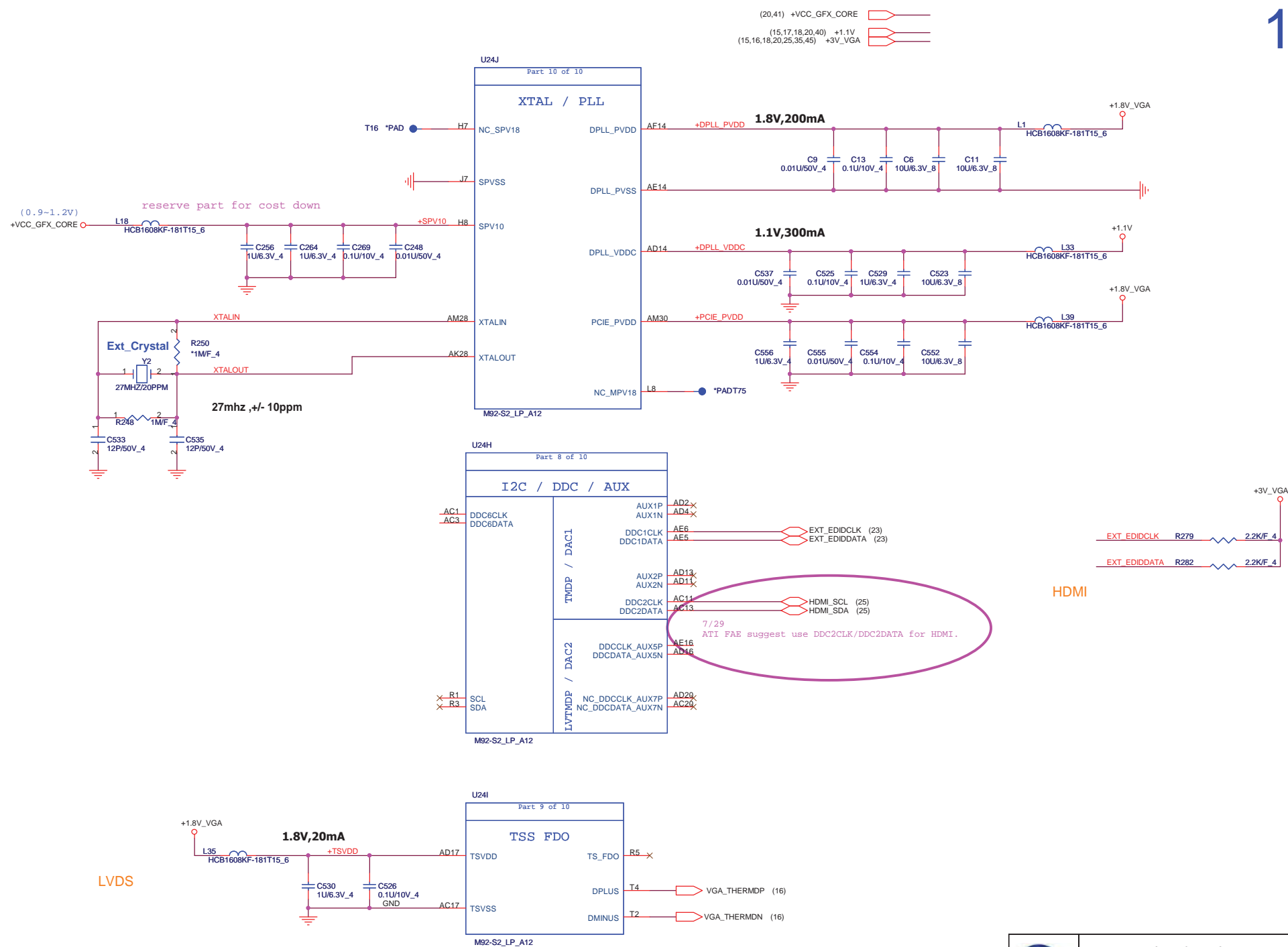
Memory Straps	DDR3	RAM_TYPE_CFG3	RAM_TYPE_CFG2	RAM_TYPE_CFG1	RAM_TYPE_CFG0
667MHz K4W1G1646D-EC15 (64M*16) Samsung		X	X	0	1
700MHz H5TQ1G63BFR-14C (64M*16) Hynix		X	X	0	0
Blank		X	X	0	0
Blank		X	X	0	0

GPIO Straps	DESCRIPTION OF DEFAULT SETTINGS	SP6 setting
GPIO0	GPIO0 - TX_PWRS_ENB (Transmitter Power Savings Enable) 0: 50% Tx output swing for mobile mode 1: full Tx output swing (Default setting for Desktop)	1
GPIO1	GPIO1 - TX_DEEMPH_EN (Transmitter De-emphasis Enable) 0: Tx de-emphasis disabled for mobile mode 1: Tx de-emphasis enabled (Default setting for Desktop)	1
GPIO2	GPIO2 - BIF_GEN2_EN (5.0 Gt/s Enable) 0: Default (Driver Controlled Gen2) 1: Strap Controlled Gen2	0
GPIO3	ATI reserved configuration straps.	0
GPIO4	ATI reserved configuration straps.	0
GPIO5	GPIO 5, AC_BATT 0: Battery saving mode = 0.0 V 1: AC (Performance mode) = 3.3 V	0
GPIO6	ATI Internal use only	0



Spread Spectrum
If U4, the discrete spread spectrum chip is not used, then pop R48 in order to pull-down BXTALOUT for EMI reasons.





PROJECT : SP6
Quanta Computer Inc.

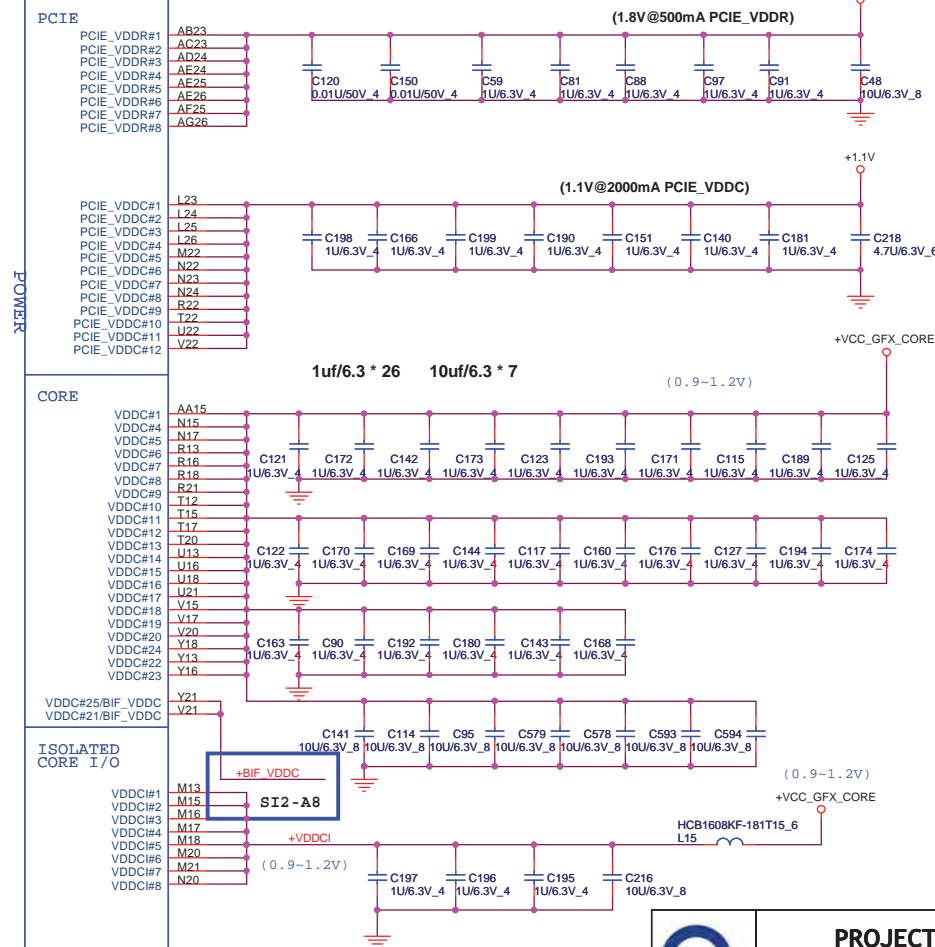
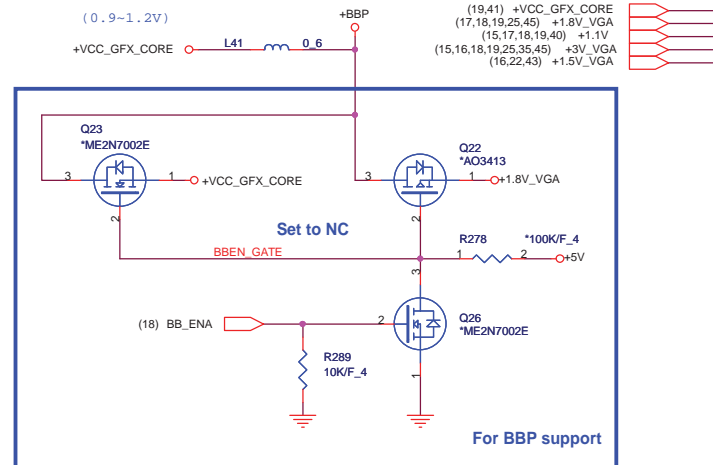
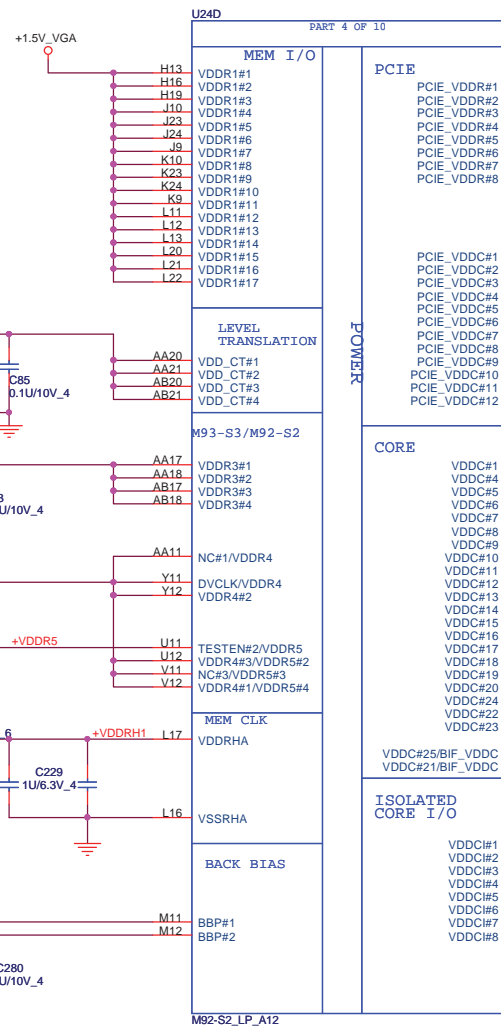
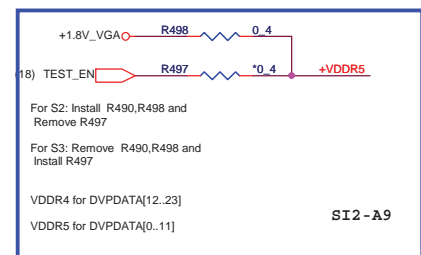
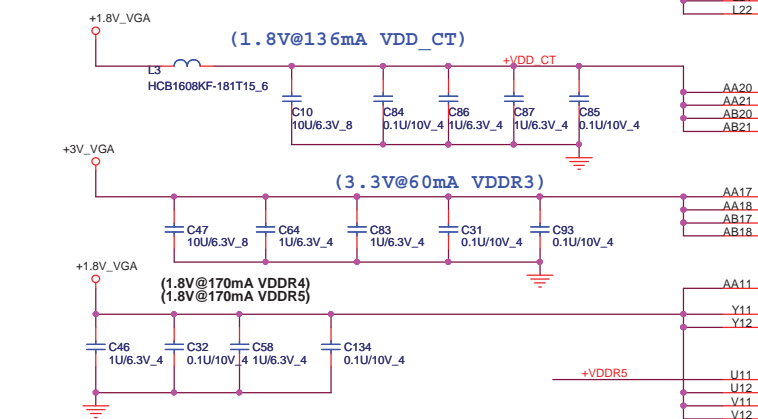
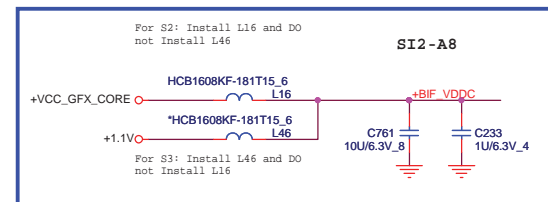
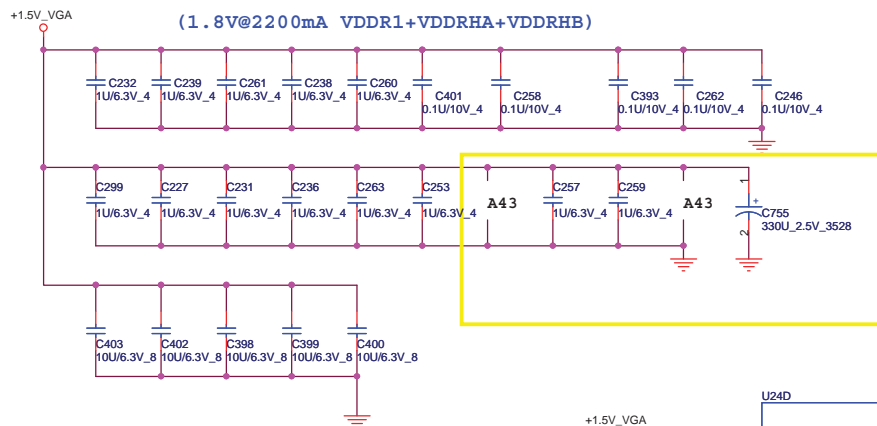
Size
Custom

Document Number
M92 XTAL/I2C

Rev
1A

Date: Tuesday, April 07, 2009

Sheet 19 of 49

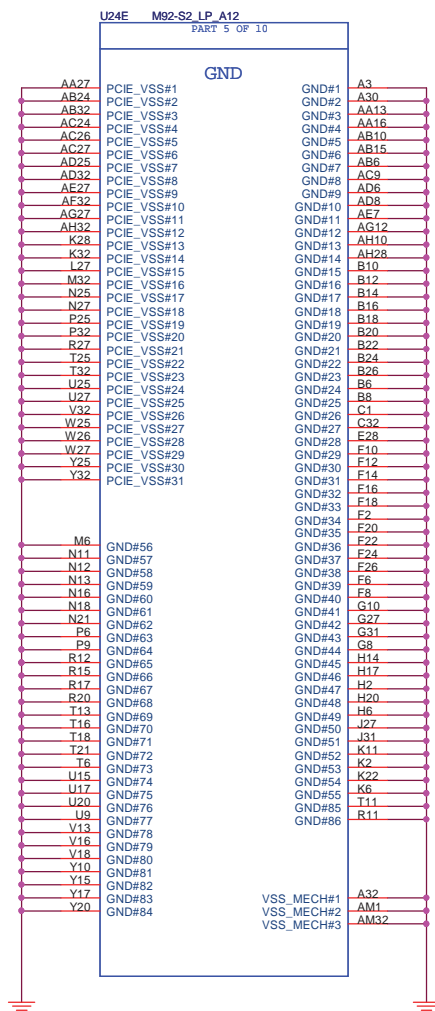


(For M96/92, 0.95V-1.1V@2A VDDCI)



PROJECT : SP6
Quanta Computer Inc.

Size	Document Number	Rev
Custom	M92 POWER/GND	2B
Date: Tuesday, April 07, 2009	Sheet 20 of 49	



Power up

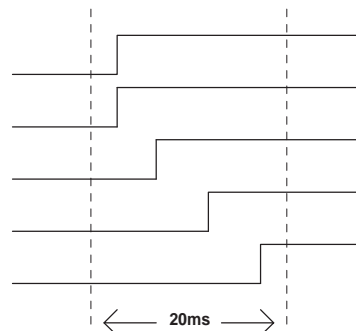
BBP/N=VDDC=VSS=3.3V

+1.1V VDDC=0.9~1.2V

DDR3(+1.5V)

VDD_CT=1.8V

VDDR3=3.3V



Power down

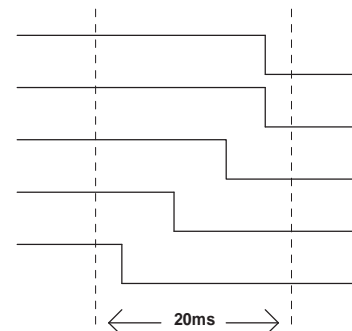
BBP/N=VDDC=VSS=3.3V

+1.1V VDDC=0.9~1.2V

DDR3(+1.5V)

VDD_CT=1.8V

VDDR3=3.3V



CONFIGURATION STRAPS

ALLOW FOR PULLUP PADS FOR THESE STRAPS AND IF THESE GPIOs ARE USED, THEY MUST NOT CONFLICT DURING RESET

RECOMMENDED SETTINGS
0= DO NOT INSTALL RESISTOR
1= INSTALL 10K RESISTOR
X= DESIGN DEPENDANT
NA= NOT APPLICABLE

STRAPS	PIN	DESCRIPTION OF DEFAULT SETTINGS	
TX_PWRS_ENB	GPIO0	PCIE FULL TX OUTPUT SWING	X
TX_DEEMPH_EN	GPIO1	PCIE TRANSMITTER DE-EMPHASIS ENABLED	X
BIF_GEN2_EN_A	GPIO2	PCIE GEN2 ENABLED	X
RSVD	GPIO8		0
BIF_VGA_DIS	GPIO9	VGA ENABLED	0
RSVD	GPIO21		0
BIOS_ROM_EN	GPIO_22_ROMCSB	ENABLE EXTERNAL BIOS ROM	X
ROMIDCFG(2:0)	GPIO[13:11]	SERIAL ROM TYPE OR MEMORY APERTURE SIZE SELECT	X X X
VIP_DEVICE_STRAP_ENA	V2SYNC	IGNORE VIP DEVICE STRAPS	X
RSVD	GENERICC		0
AUD[1]	HSYNC	AUD[1] AUD[0]	0
AUD[0]	VSYN	AUD[1] AUD[0] 0 0 No audio function 0 1 Audio for DisplayPort and HDMI if dongle is detected 1 0 Audio for DisplayPort only 1 1 Audio for both DisplayPort and HDMI	X X



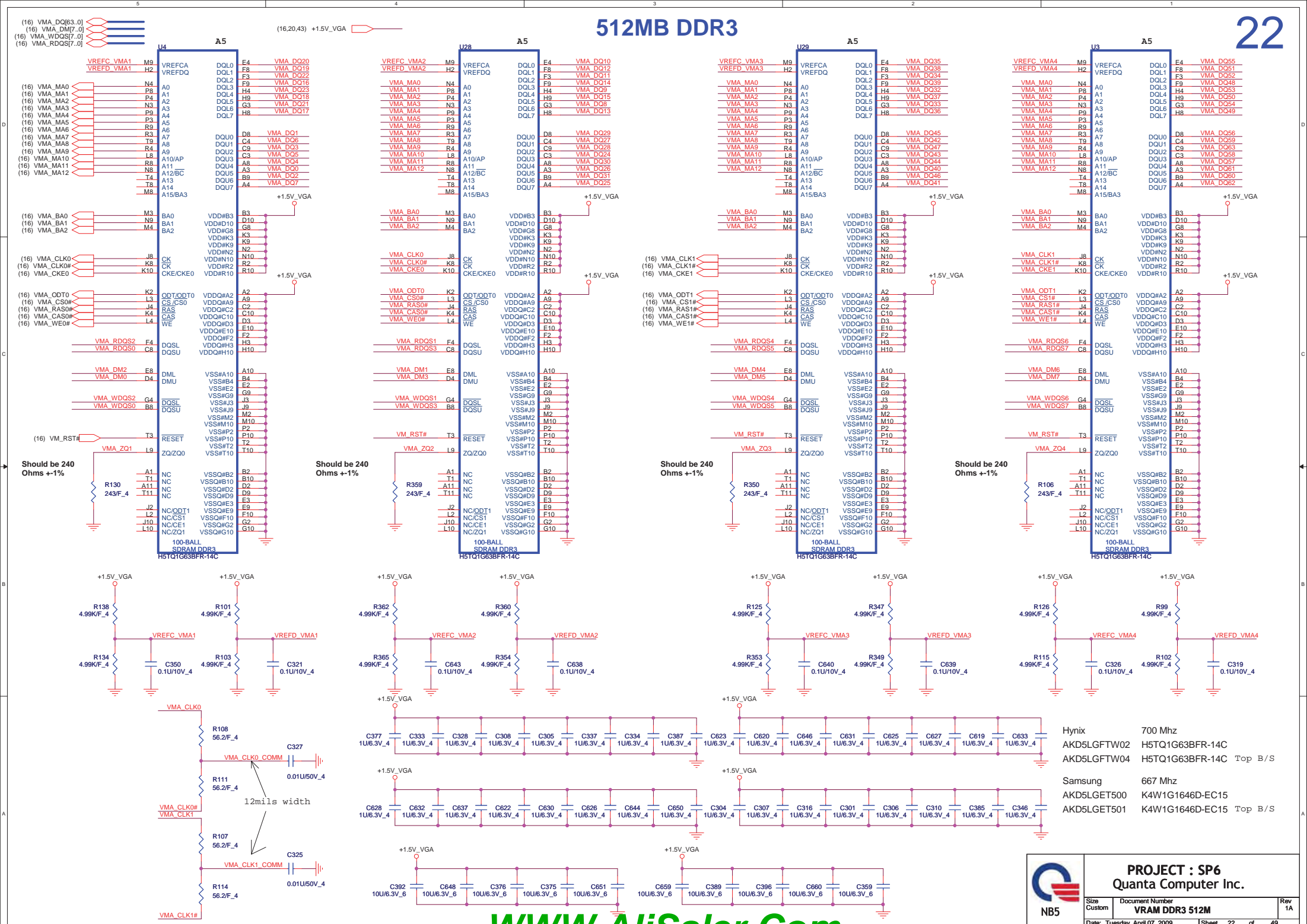
PROJECT : SP6
Quanta Computer Inc.

Size
Custom

Document Number
M92-S2-LP_G_GND

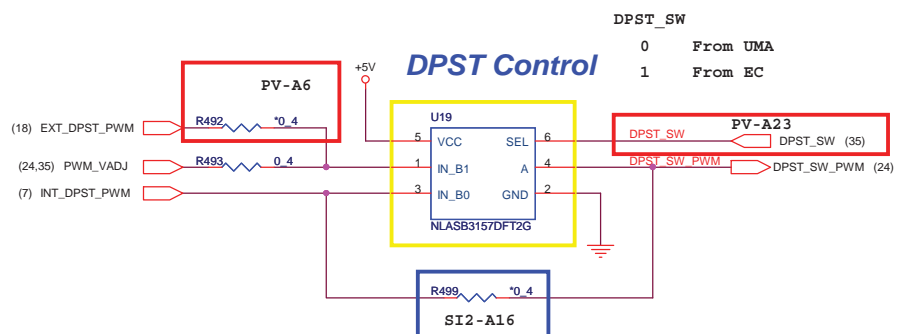
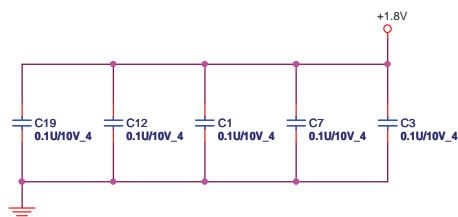
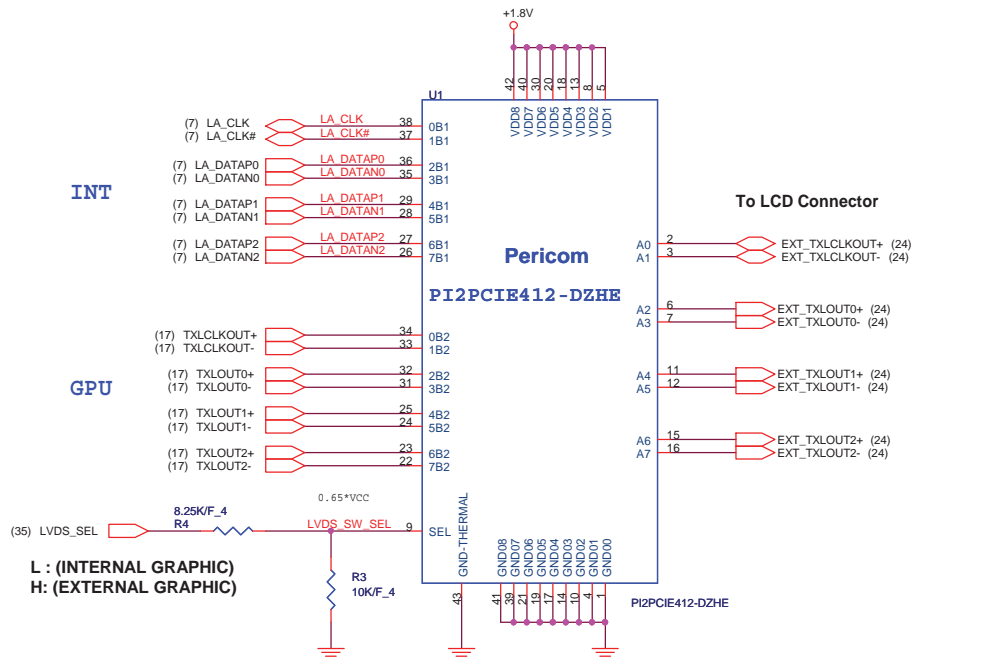
Rev
1A

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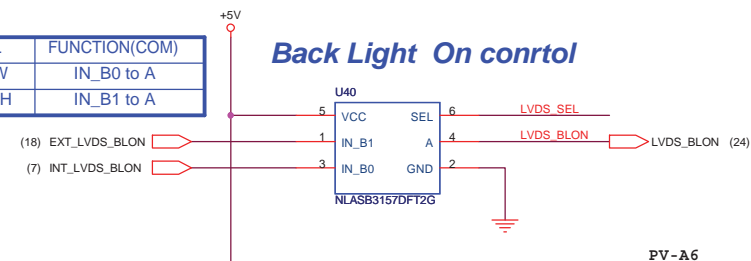


INT

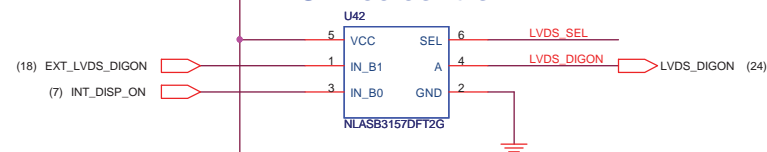
GPU



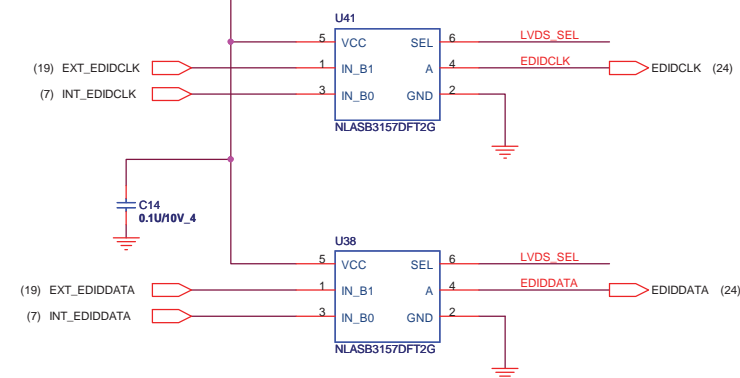
Back Light On control



LCDVcc control



LCD EDID(CLK/DATA) control



PROJECT : SP6
Quanta Computer Inc.

Size Custom Document Number
NB5 LVDS Hyper_Switch

Rev 1A

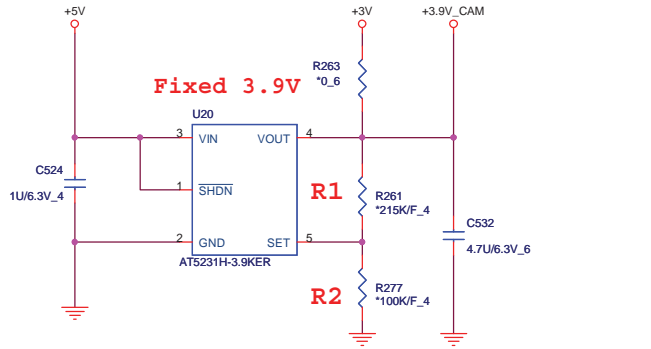
Date: Tuesday, April 07, 2009 Sheet 23 of 49

USB CAMERA /DIGITAL MIC CONNECT

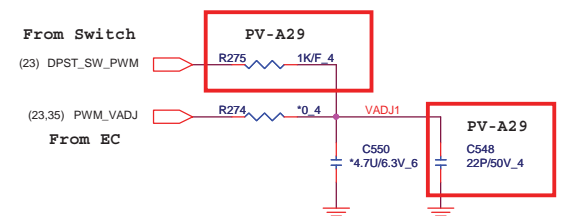
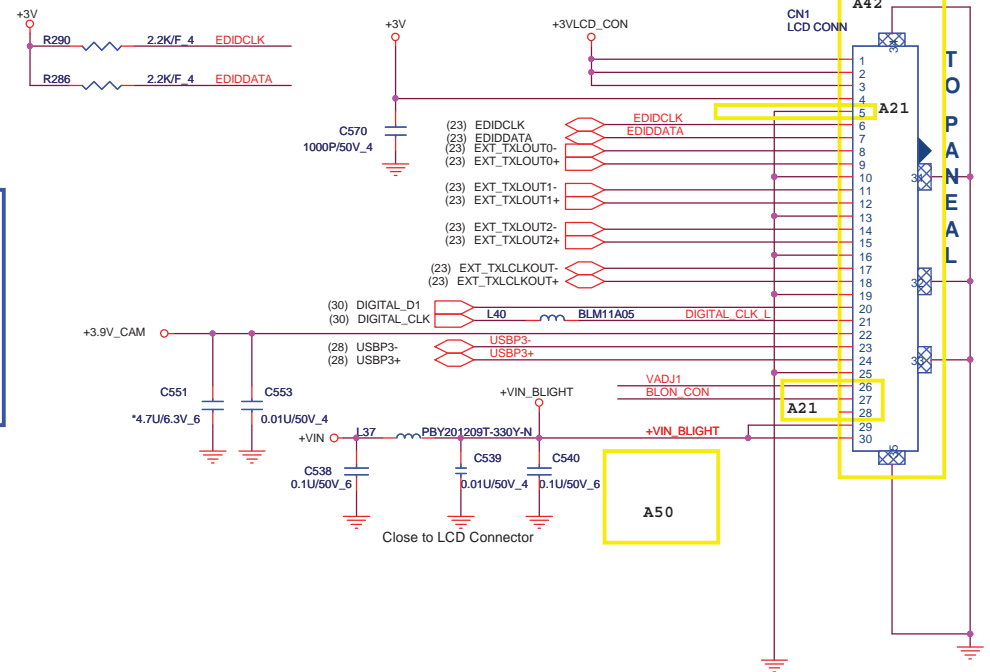
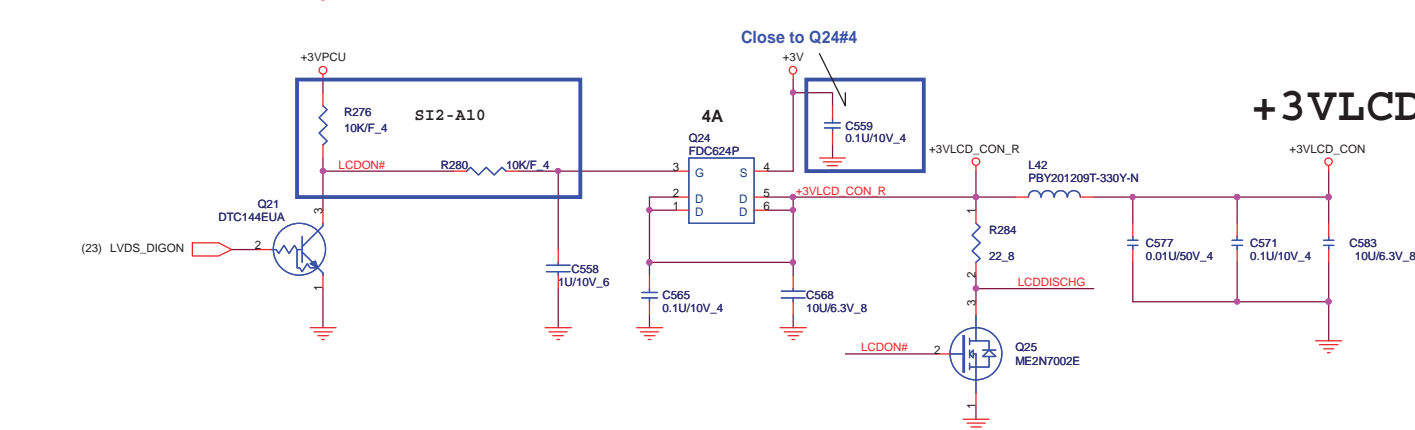
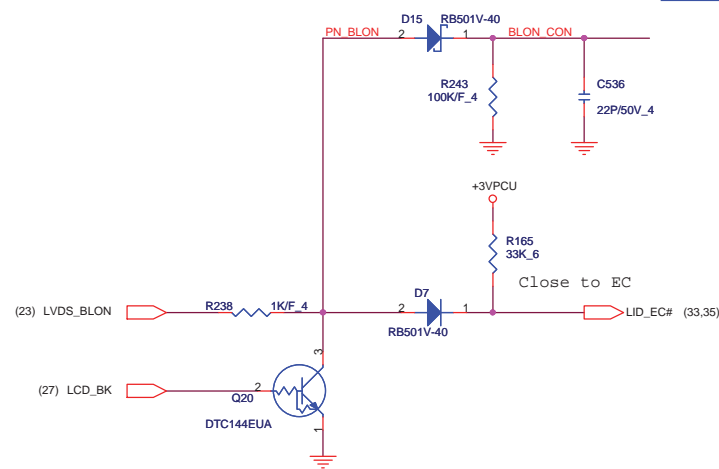
(2,4,7,10,12,14,25,26,27,28,29,30,31,32,33,34,35,36,45)
 (+3V)
 (20,23,25,29,31,36,45)
 (37,39,40,41,42,43,44,45)
 (+5V)
 (+VIN)
 (26,32,33,35,37,38,39,43,44,45)
 (+3VPCU)

24

$$V_{out}=1.25(1+R1/R2)$$



Add for EMI solution
 DIGITAL_CLK_L
 C560 10P/50V_4
 Close to LCD#21

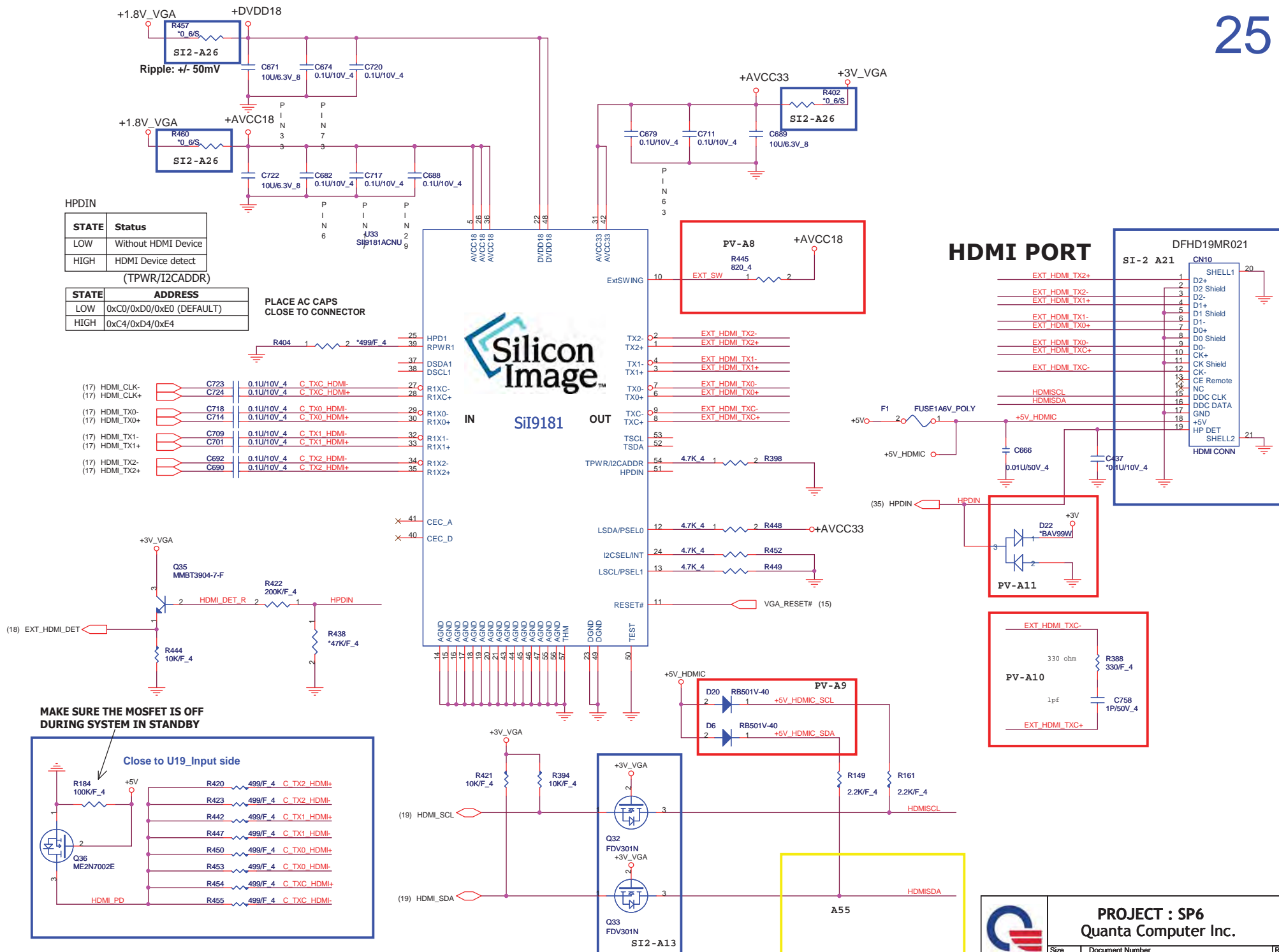


+3VLCD



PROJECT : SP6 Quanta Computer Inc.		
Size Custom	Document Number LVDS/Camera/Digital MIC	Rev 1A
Date: Tuesday, April 07, 2009	Sheet 24	of 49

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XOR Chain Entrance Strap

ICH9-M LAN100_SLP Strap
(Internal VR for
VccLAN1_05 and
VccCL1.05)

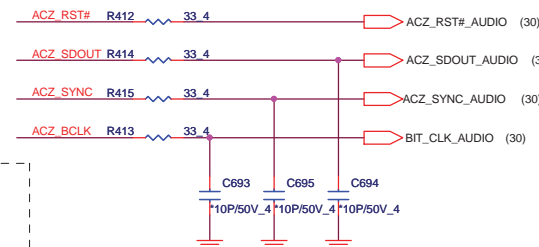
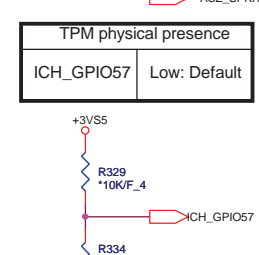
LAN100_SLP	Low = Internal VR disabled High = Internal VR enable(Default)
------------	--

ICH9 Boot BIOS select

(default)

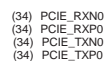
ACZ_SPKR

TPM physical presence	
ICH_GPIO57	Low: Default



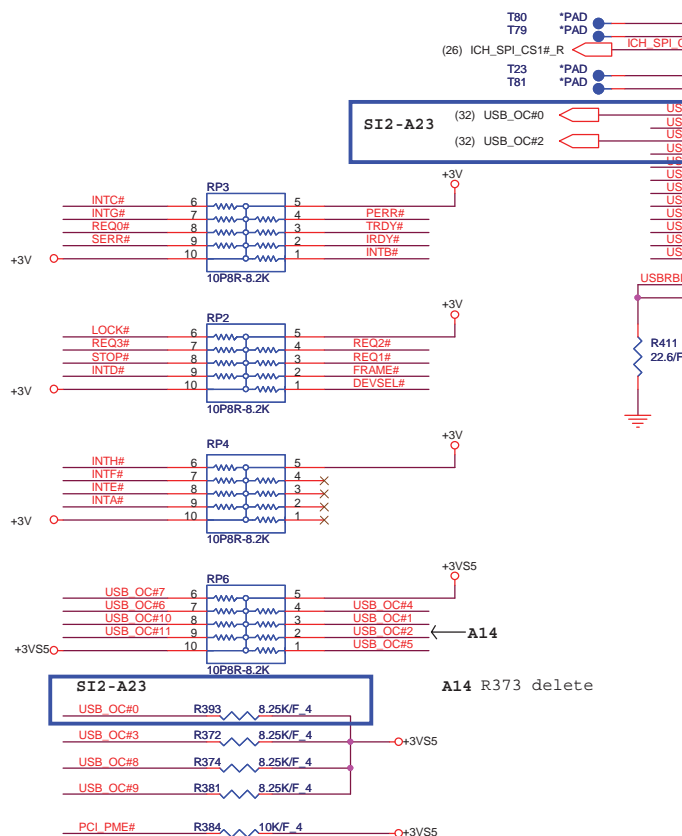


Size Custom	Document Number ICH9 (PM/GPIO)	Rev 1A
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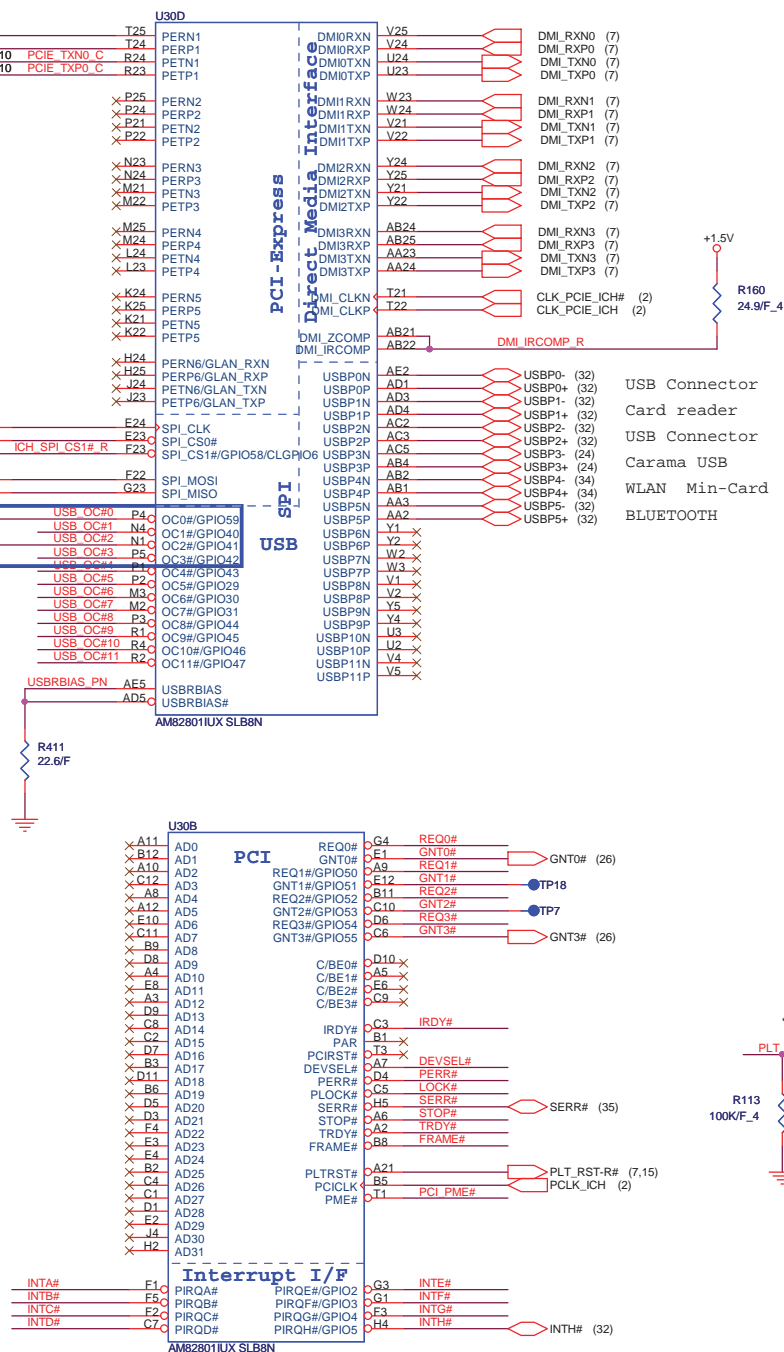


Delete R121,R378 due have the same pd in page 26

A3



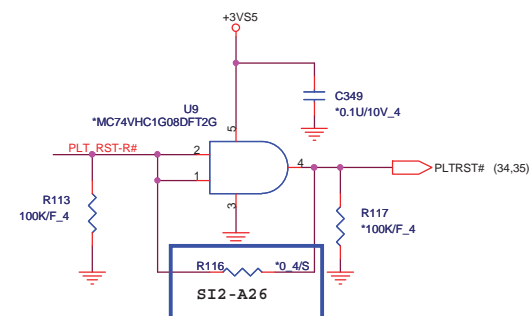
A14 R373 delete



USB port

Assignment function

USB0	USB Connector
USB1	USB Card reader
USB2	USB Connector
USB3	USB Camera module
USB4	Reserve for Mini Card
USB5	Bluetooth
USB6	NA
USB7	NA
USB8	NA
USB9	NA
USB10	NA
USB11	NA

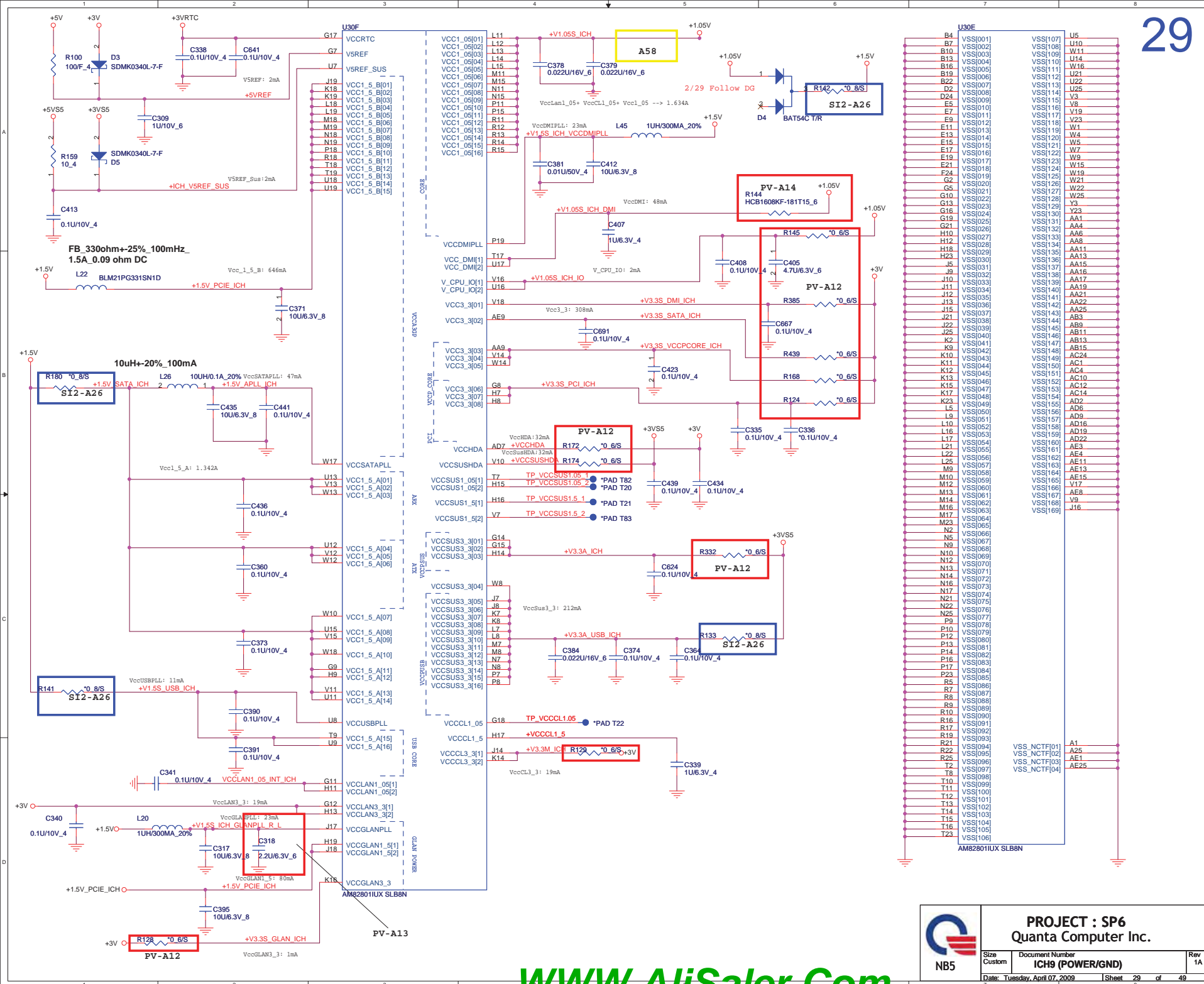


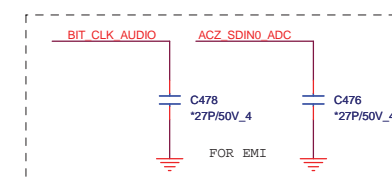
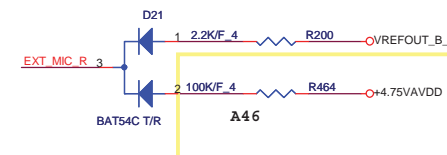
PROJECT : SP6
Quanta Computer Inc.

Size	Custom
------	--------

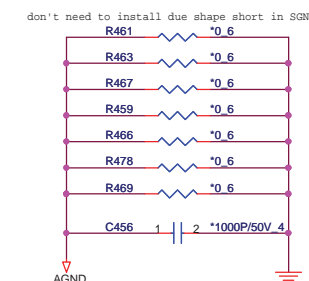
Document Number	ICH9 (USB/PCIE/DMI)
-----------------	----------------------------

Rev	14
-----	----

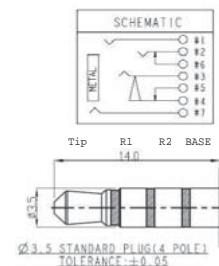
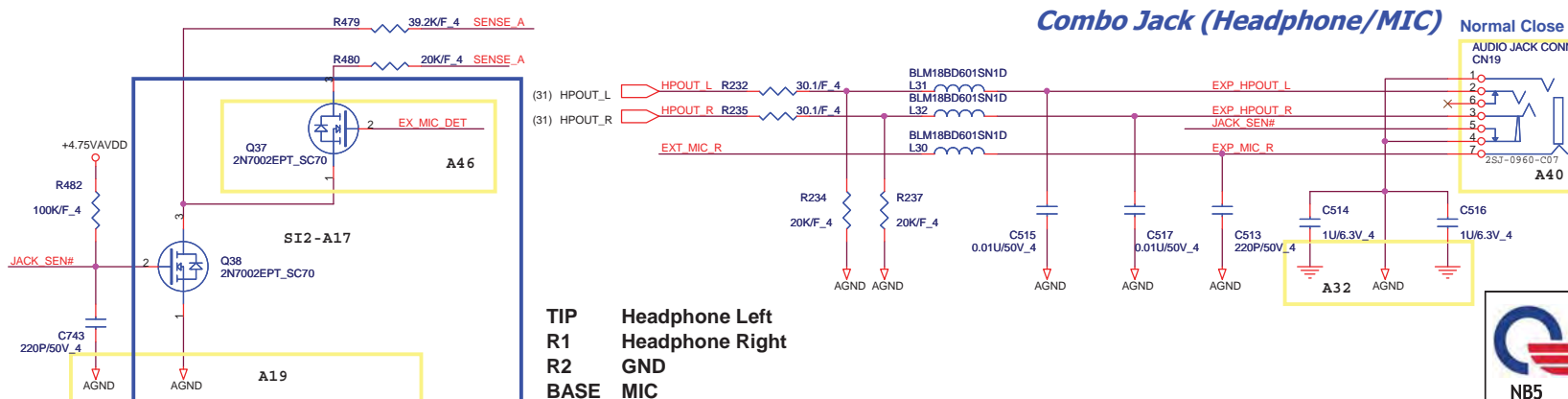




SA_A# -->Headphone out
SA_B# -->EXT MIC



Normal Close		
SI-1	JACK_SEN#	EX_MIC_DET
None	Low	HIGH
Normal HP	HIGH	Low
HP+MIC	HIGH	HIGH

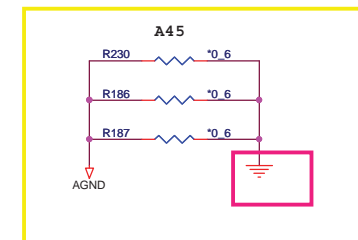


31

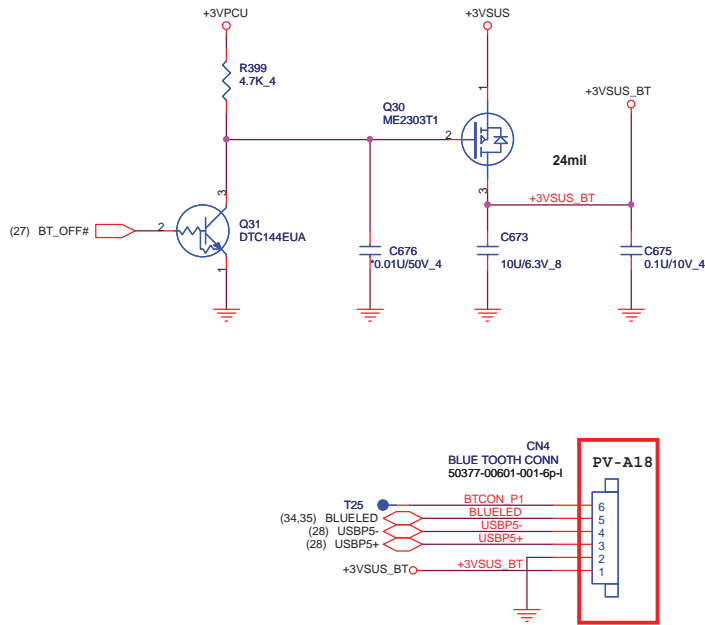
[illegible]

GAIN0	GAIN1	AV	RIN
0	0	6dB	90K
0	1	10dB	70K
1	0	15.6dB	45K
1	1	21.6dB	25K

```
TPA6047A4    High enable
```

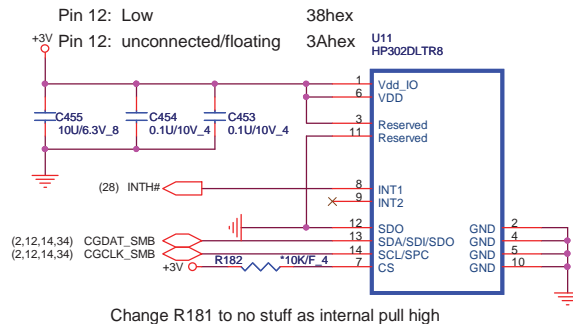


BLUETOOTH

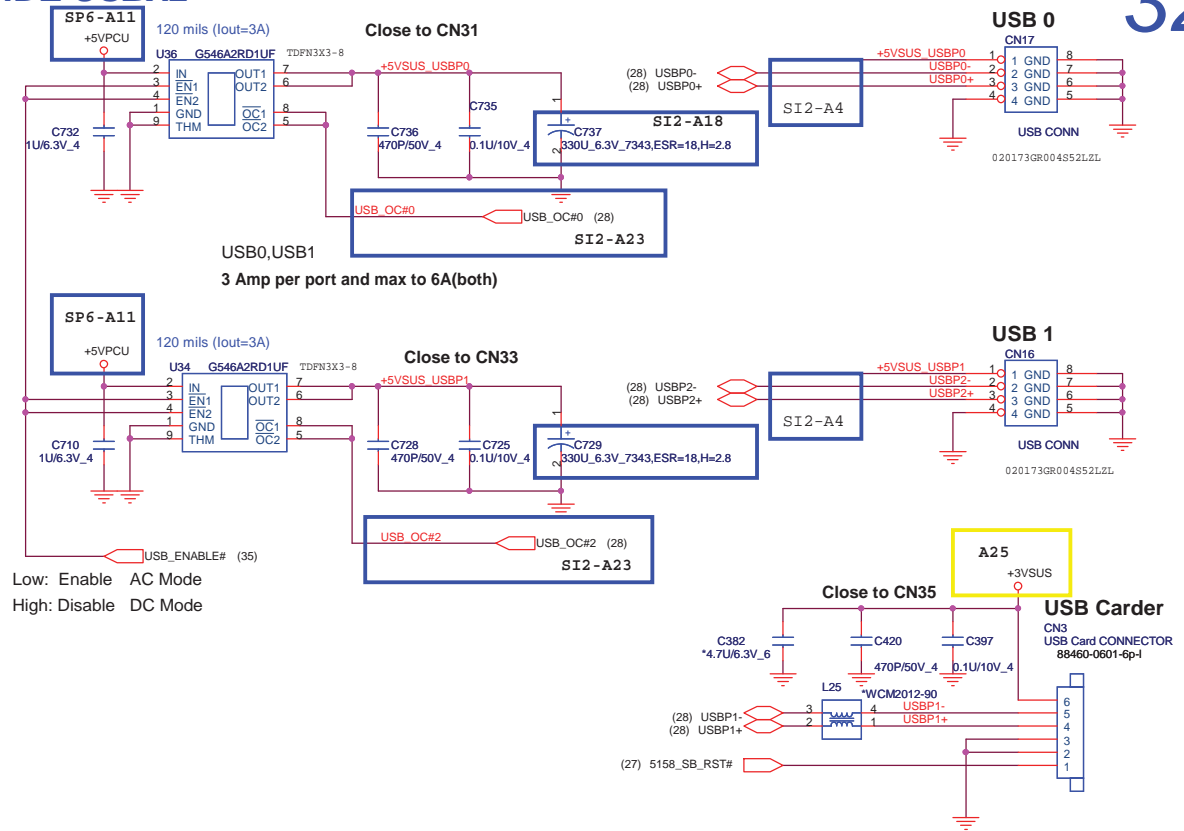


Accelerometer Sensor

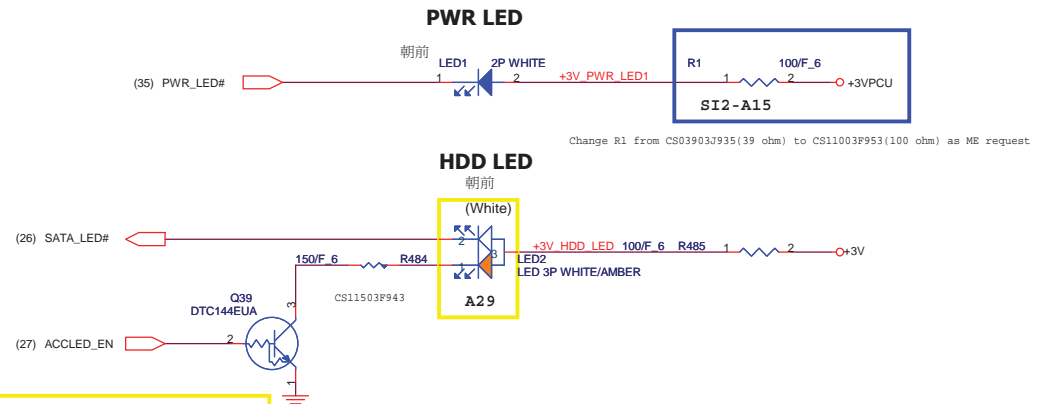
SGT-LIS302DLTR interrupt pin default is low / active Hi, BIOS need to programming 22h to change status from active Hi to low



LEFT SIDE USBX2



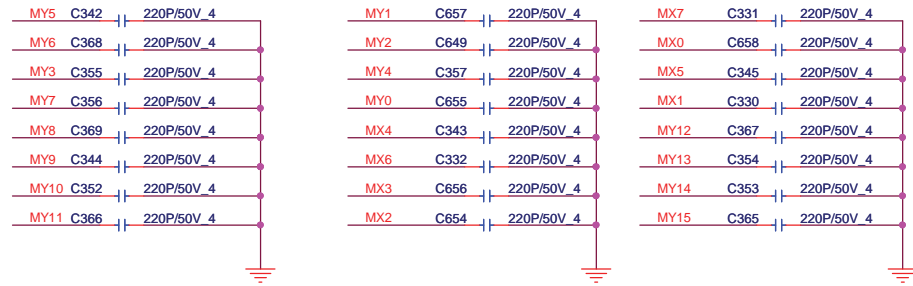
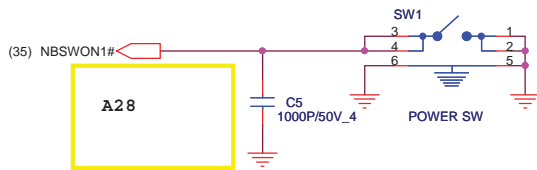
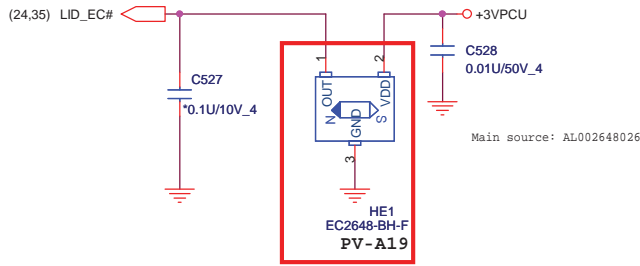
LED



use a single 80 mil copper fill between digital ground and audio ground. None of the 0 ohm resistors should be used.

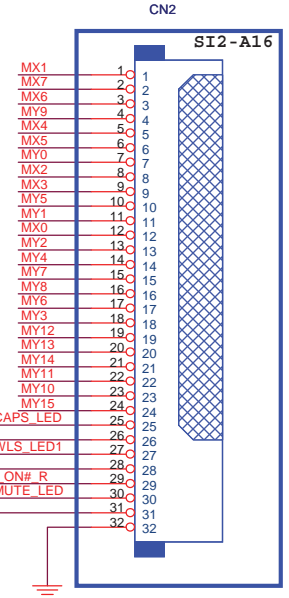
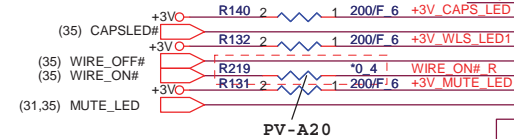
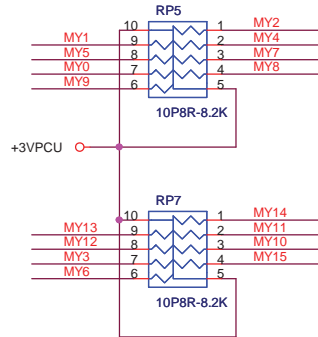


NB5	PROJECT : SP6 Quanta Computer Inc.		
	Size Custom	Document Number BT/WC/FP/USB/G-sensor	Rev 1A
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Modify CN2 footprint from bl137-32r1-tand-32p-1 to 196033-32041-32p-1

KEYBOARD PULL-UP

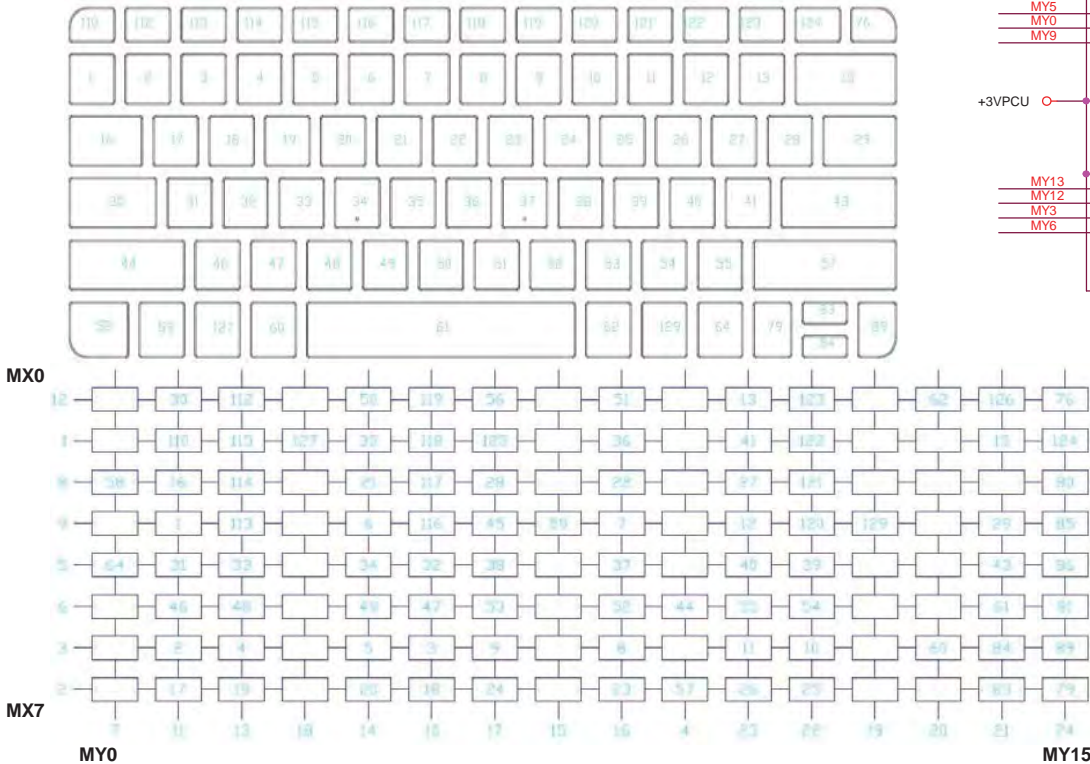


KB CONN

Caps Lock LED 2 Pins 30mA
Wireless LED 3 pins
MUTE LED 2 Pins
Charge LED 3 pins

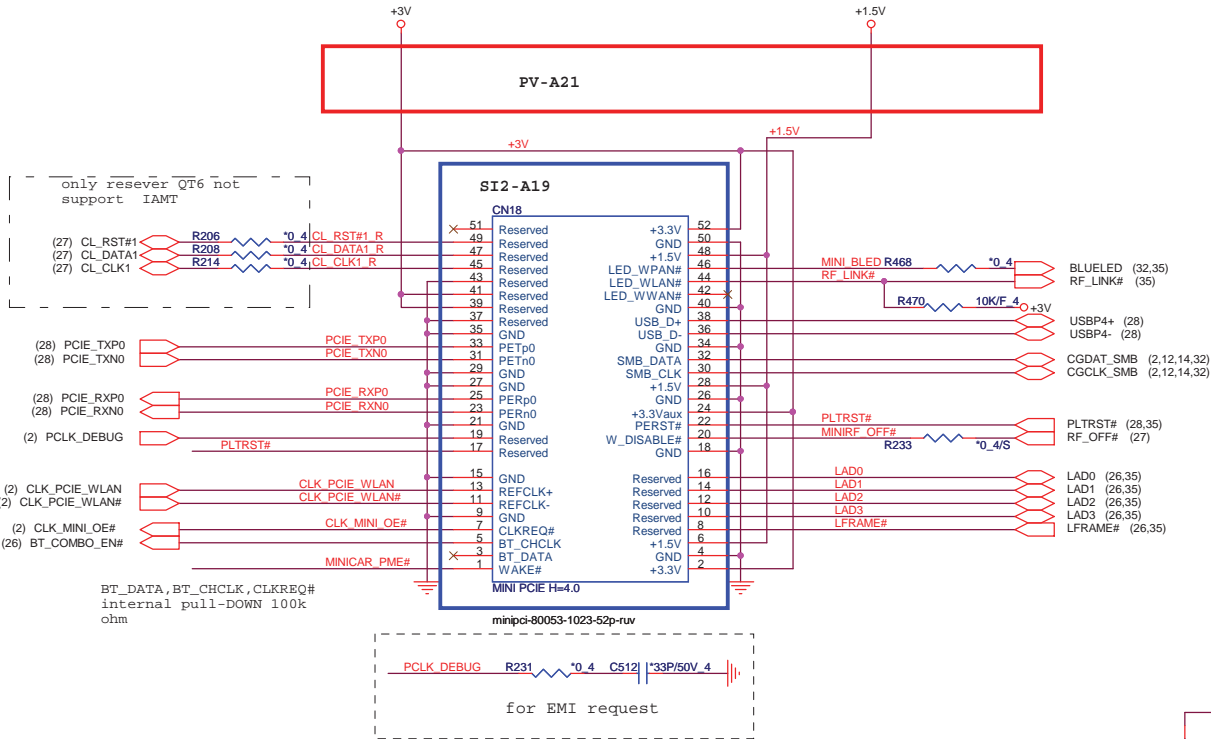
Pin 32

Pin 1

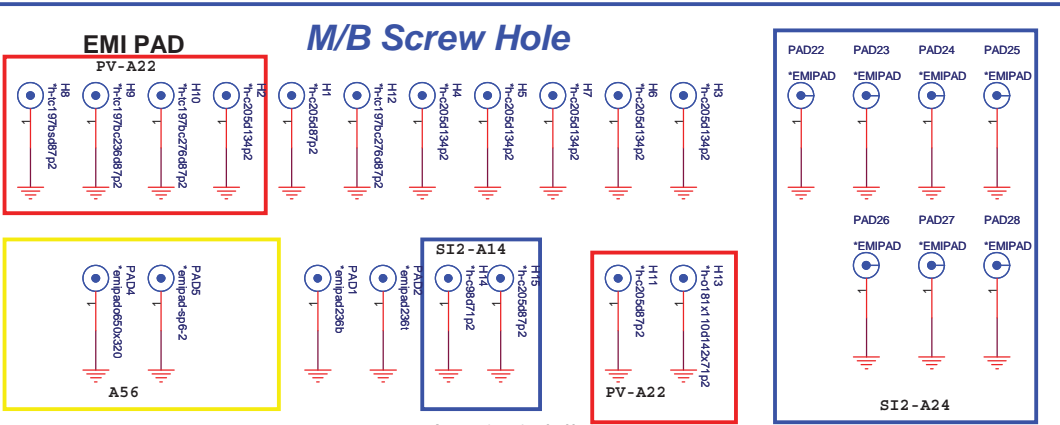


PROJECT : SP6
Quanta Computer Inc.

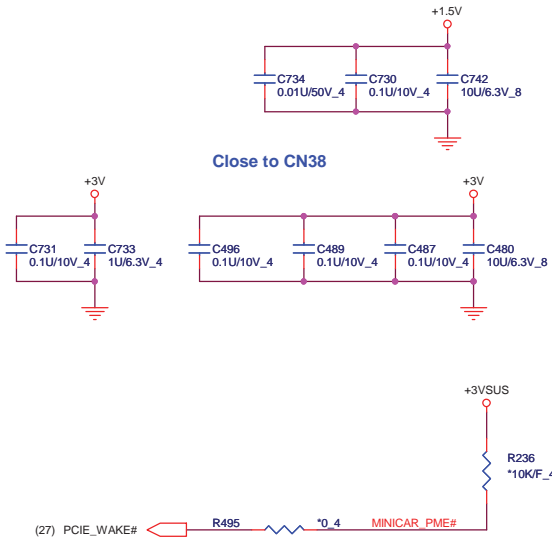
Size B	Document Number	Rev
	KB/CAP	1A
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Modify CN18 footprint from from minipci-asob241-r40n-7h-52p-ruv to minipci-80053-1023-52p-ruv

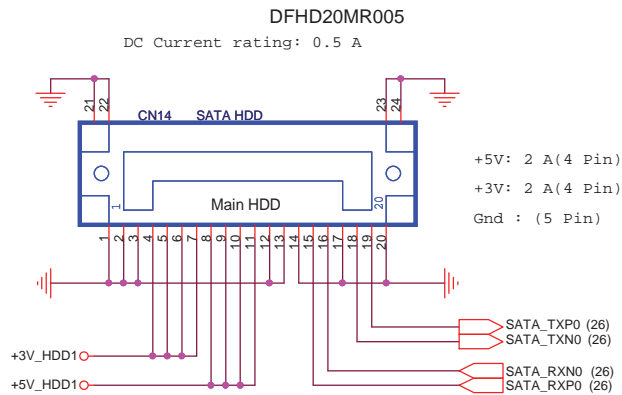


Delete PAD3, PAD6 and add H15



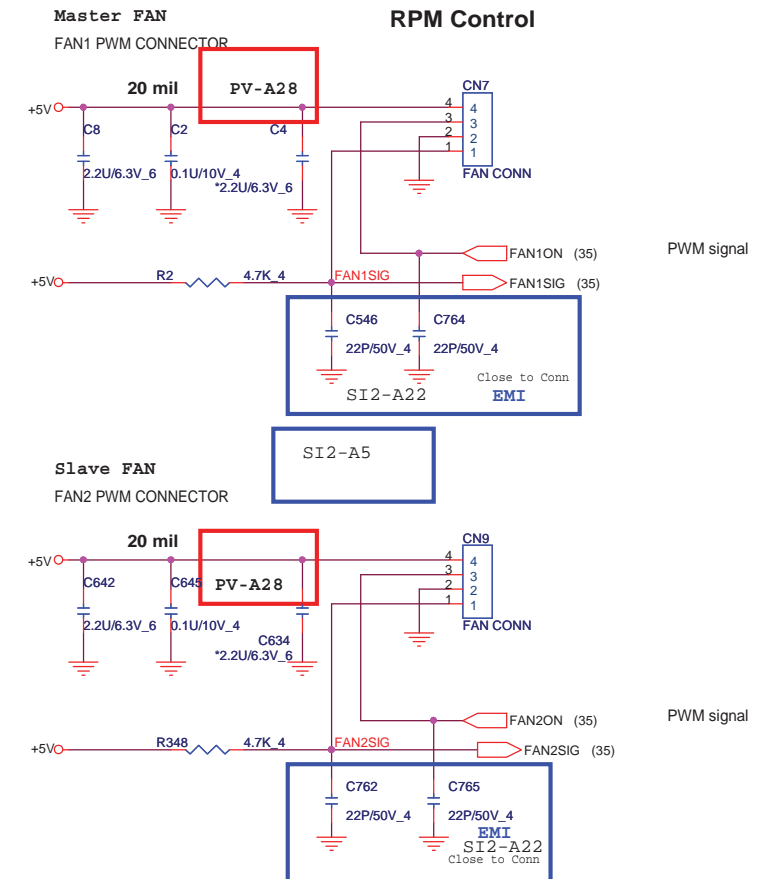
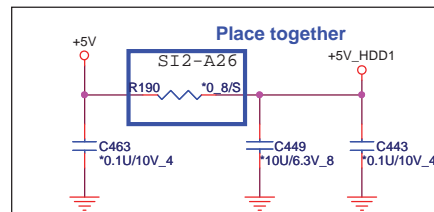
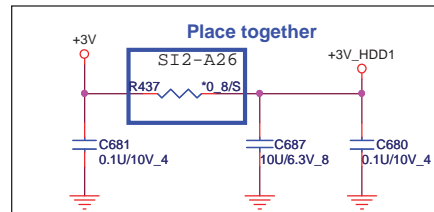


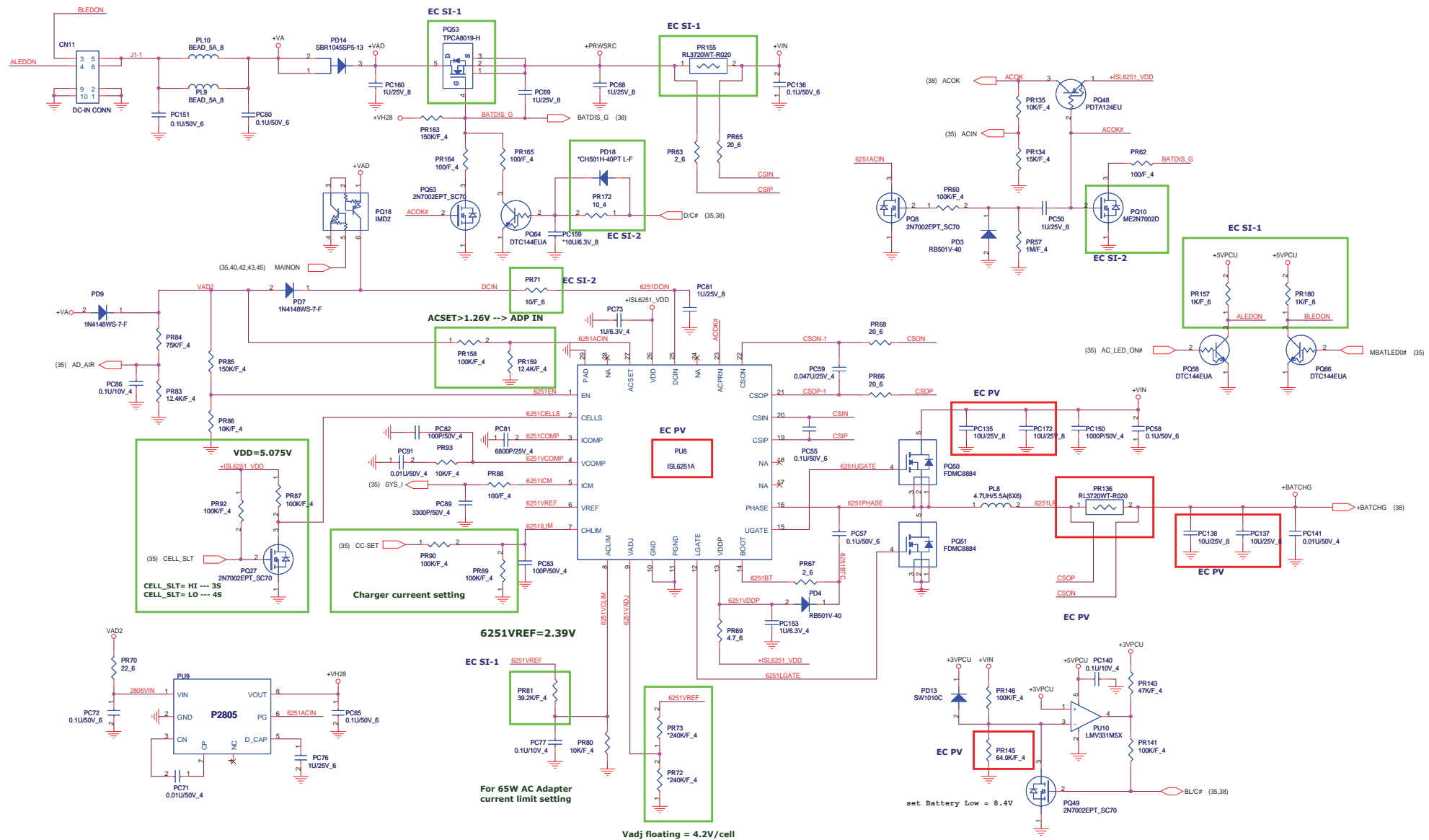
SATA 1.8" HDD CONNECTOR

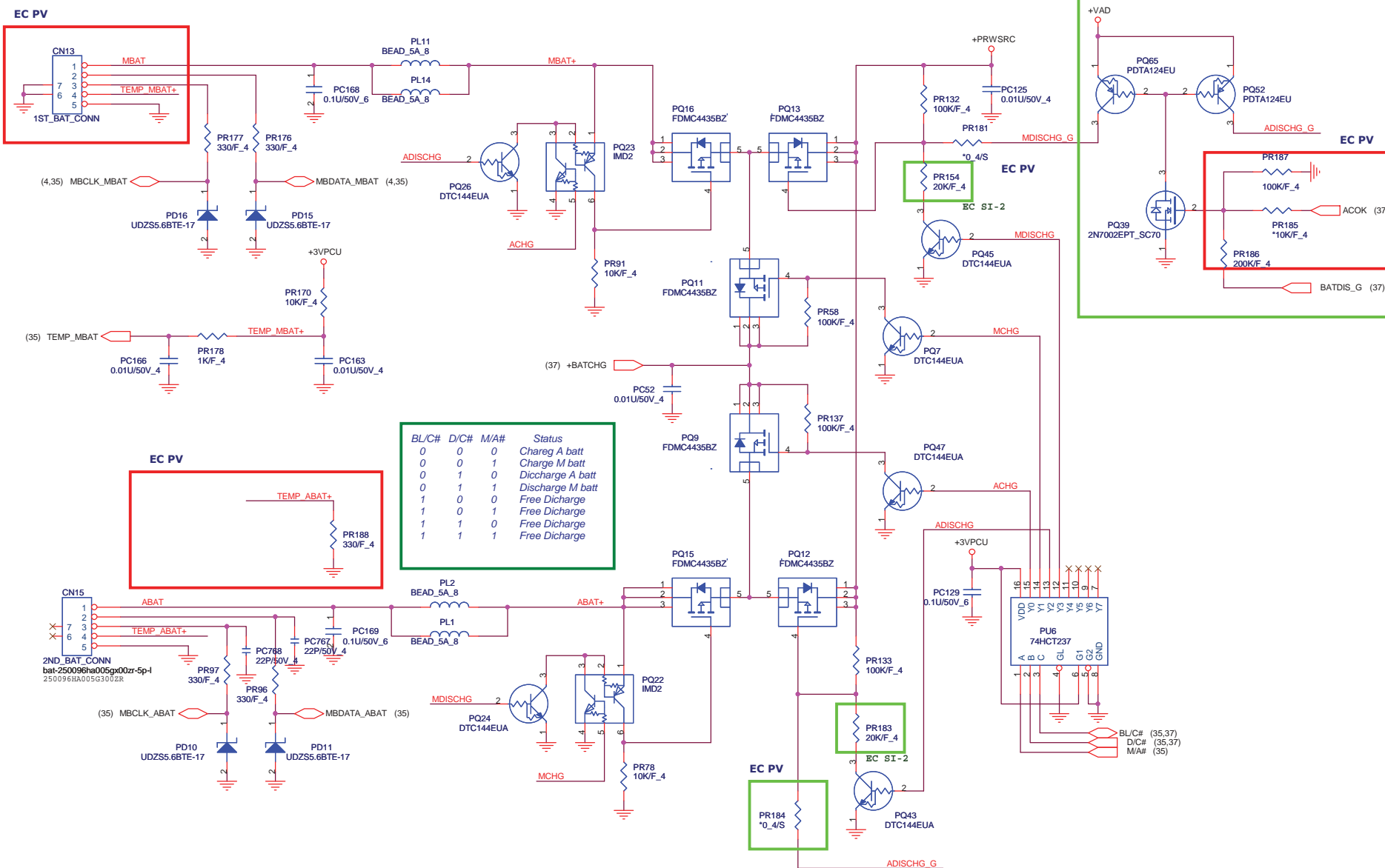


Modify 固定孔 Size as SMT request

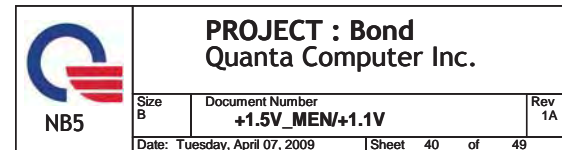
GS12201-1011-9F-20P-L-QT6

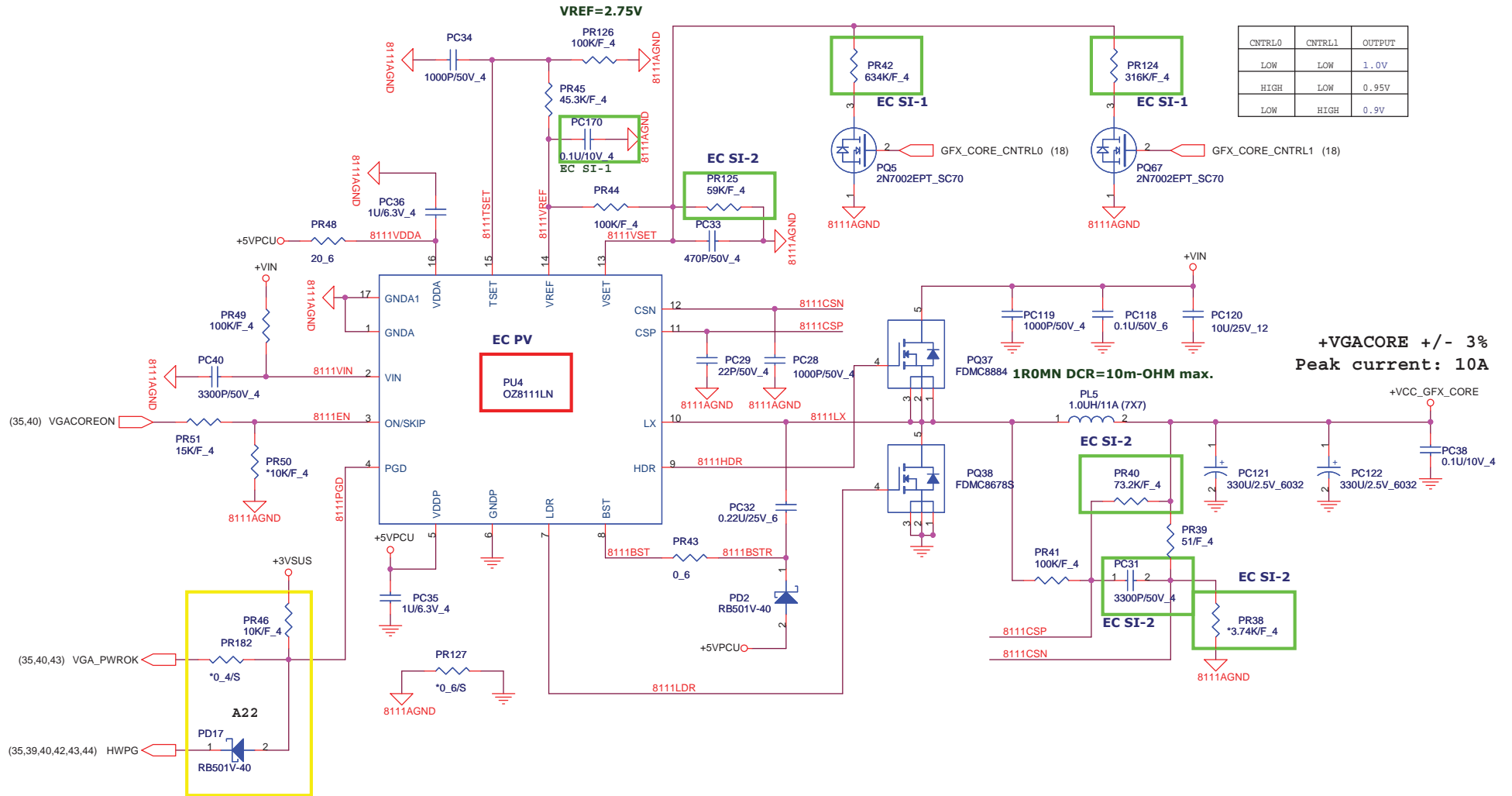






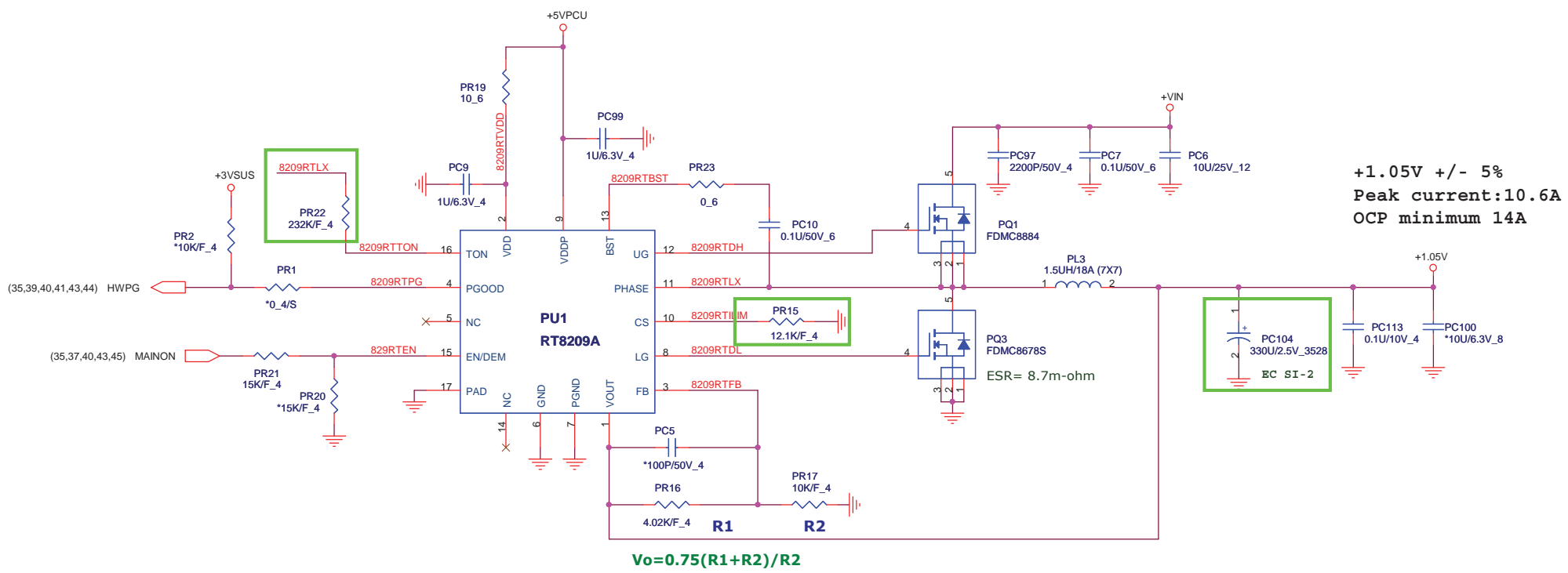







**PROJECT : Bond
Quanta Computer Inc.**

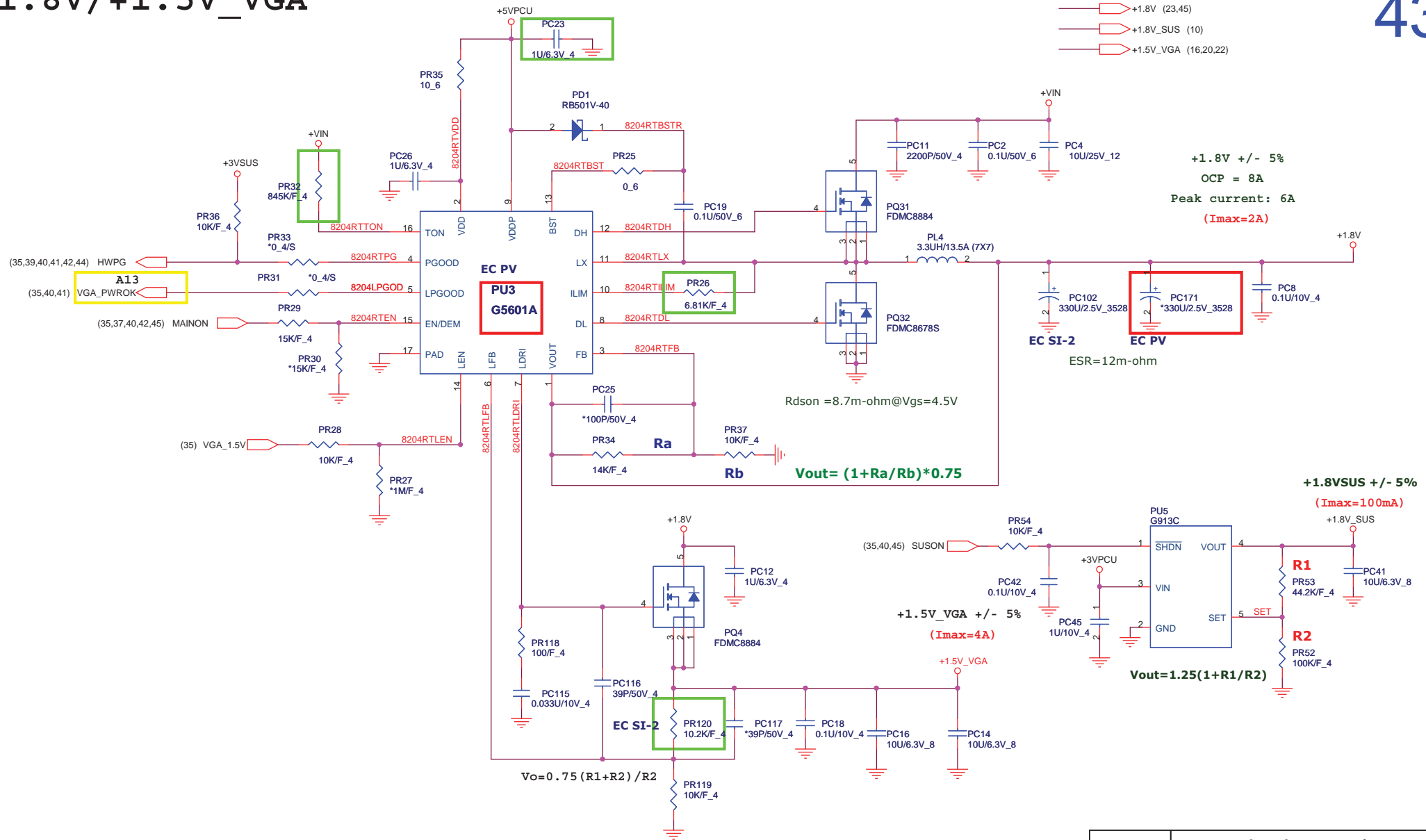
Size B	Document Number VGACORE	Rev 1A
Date: Tuesday, April 07, 2009		Sheet 41 of 49



			PROJECT : Bond Quanta Computer Inc.		
Size B	Document Number +1.05V/+1.5V				Rev 1A
Date: Tuesday, April 07, 2009			Sheet 42 of 49		

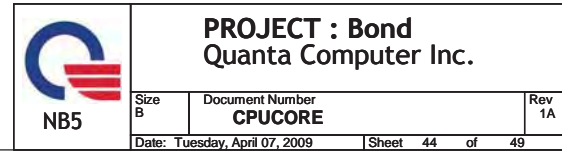
+1.8V/+1.5V_VGA

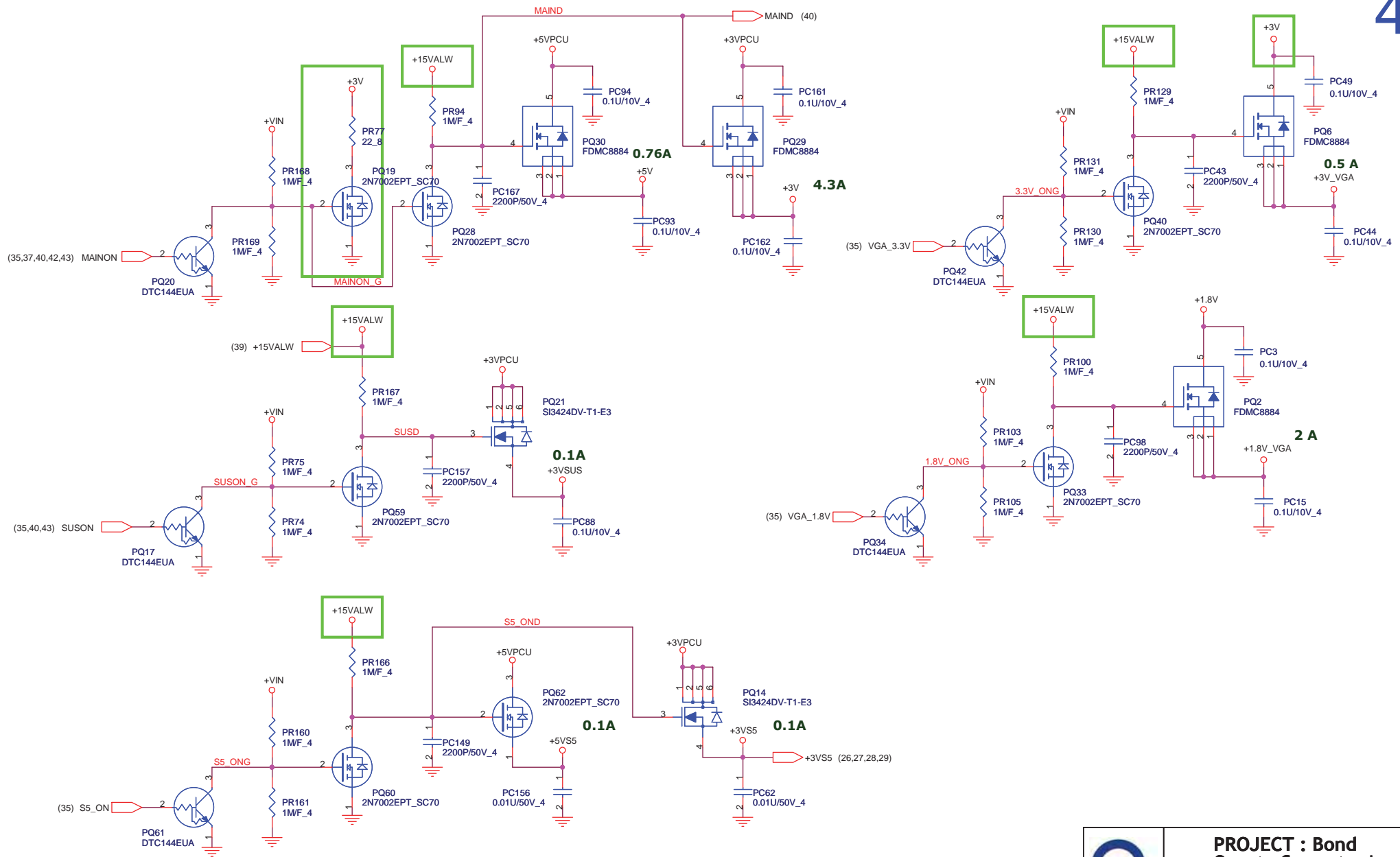
43




**PROJECT : Bond
Quanta Computer Inc.**

Size	Document Number	Rev
B	+1.05V/+1.5V	1A
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		PROJECT : Bond	
		Quanta Computer Inc.	
Size B	Document Number DISCHARGER	Rev 1A	
Date: Tuesday, April 07, 2009		Sheet 45 of 49	